PMN27XPE

20 V, single P-channel Trench MOSFET 20 September 2012

Product data sheet

Product profile

1.1 General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Fast switching
- Trench MOSFET technology
- 2 kV ESD protection

1.3 Applications

- Relay driver
- High-speed line driver
- High-side loadswitch
- Switching circuits

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _{amb} = 25 °C		-	-	-20	V
V_{GS}	gate-source voltage			-12	-	12	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	-5.7	Α
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I_D = -3 A; T_j = 25 °C		-	27	30	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².





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2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain	<u> </u>	D I
2	D	drain		
3	G	gate		$G \left(\begin{array}{c} & & & & \\ & & & & \\ & & & & \\ & & & & $
4	S	source	TSOP6 (SOT457)	
5	D	drain		
6	D	drain		S 017aaa259

3. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMN27XPE	TSOP6	plastic surface-mounted package (TSOP6); 6 leads	SOT457			

4. Marking

Table 4. Marking codes

Type number	Marking code
PMN27XPE	WC

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _{amb} = 25 °C		-	-20	V
V_{GS}	gate-source voltage			-12	12	V
I _D	drain current	$V_{GS} = -4.5 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}; t \le 5 \text{ s}$	[1]	-	-5.7	Α
		V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-4.4	Α
		V _{GS} = -4.5 V; T _{amb} = 100 °C	[1]	-	-3.5	Α
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10$ μs		-	-22	Α
P _{tot}	total power dissipation	T _{amb} = 25 °C	<u>[2]</u>	-	530	mW
			[1]	-	1250	mW
		T _{sp} = 25 °C		-	8330	mW

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Symbol	Parameter	Conditions		Min	Max	Unit
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain o	diode		,	•		
I _S	source current	T _{amb} = 25 °C	[1]	-	-1.3	Α
ESD maximum rating						
V _{ESD}	electrostatic discharge voltage	НВМ	[3]	-	2000	V

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Measured between all pins.

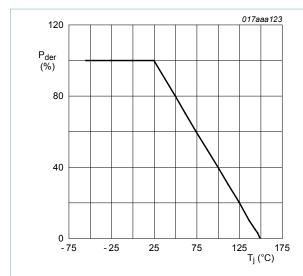


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

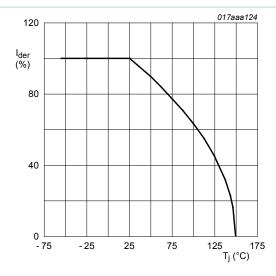


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}\text{C})}} \times 100 \%$$

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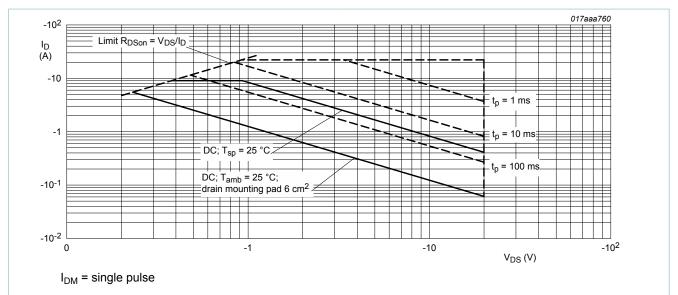


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
(y d)	thermal resistance	in free air	[1]	-	206	237	K/W
	from junction to ambient		[2]	-	86	100	K/W
	ambient		[3]	-	52	60	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	13	15	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm²
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm², $t \le 5$ s

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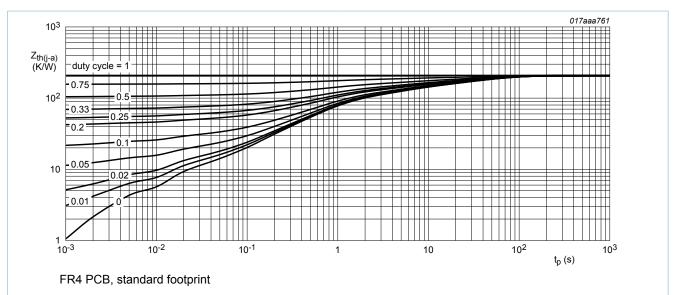


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

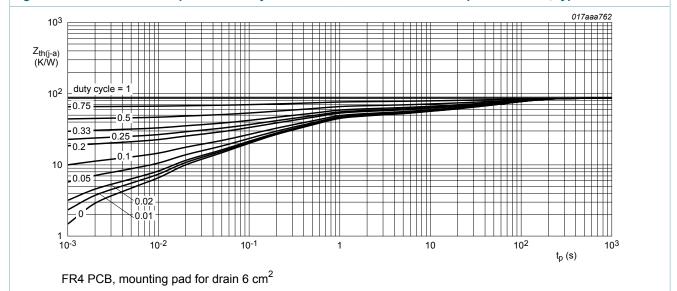


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

7. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Static chara	Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = -250 \mu A; V_{GS} = 0 V; T_j = 25 °C$		-20	-	-	V	
V _{GSth}	gate-source threshold voltage	I_D = -250 μ A; V_{DS} = V_{GS} ; T_j = 25 °C		-0.75	-1	-1.25	V	
I _{DSS}	drain leakage current	V_{DS} = -20 V; V_{GS} = 0 V; T_j = 25 °C		-	-	-1	μA	
I _{GSS}	gate leakage current	V _{GS} = 12 V; V _{DS} = 0 V; T _j = 25 °C		-	-	10	μA	
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V_{GS} = -12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-10	μA
R _{DSon}	drain-source on-state	V_{GS} = -4.5 V; I_D = -3 A; T_j = 25 °C	-	27	30	mΩ
	resistance	V_{GS} = -4.5 V; I_D = -3 A; T_j = 150 °C	-	56	64	mΩ
		V_{GS} = -2.5 V; I_D = -3 A; T_j = 25 °C	-	39	44	mΩ
9 _{fs}	forward transconductance	V_{DS} = -10 V; I_{D} = -3 A; T_{j} = 25 °C	-	16	-	S
Dynamic c	haracteristics					
Q _{G(tot)}	total gate charge	V_{DS} = -10 V; I_{D} = -3 A; V_{GS} = -4.5 V;	-	15	22.5	nC
Q_{GS}	gate-source charge	T _j = 25 °C	-	3	-	nC
Q_{GD}	gate-drain charge		-	3	-	nC
C _{iss}	input capacitance	$V_{DS} = -10 \text{ V; } f = 1 \text{ MHz; } V_{GS} = 0 \text{ V;}$	-	1770	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	254	-	pF
C _{rss}	reverse transfer capacitance		-	180	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -10 V; I_{D} = -3 A; V_{GS} = -4.5 V;	-	15	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	22	-	ns
t _{d(off)}	turn-off delay time		-	37	-	ns
t _f	fall time		-	29	-	ns
Source-dra	in diode	,	1	1		
V _{SD}	source-drain voltage	I_S = -1.3 A; V_{GS} = 0 V; T_j = 25 °C	-	-0.7	-1.2	V

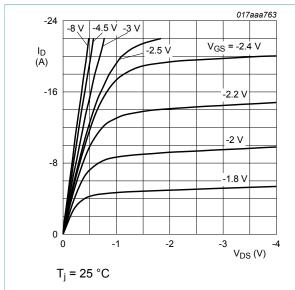
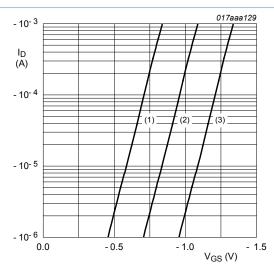


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values



 $T_j = 25 \,^{\circ}C; \, V_{DS} = -3 \,^{\circ}V$

- (1) minimum values
- (2) typical values
- (3) maximum values

Fig. 7. Sub-threshold drain current as a function of gate-source voltage

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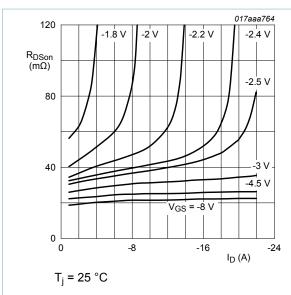


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

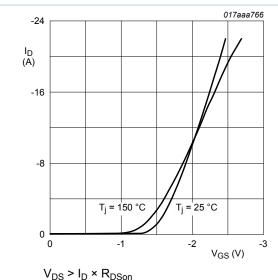


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

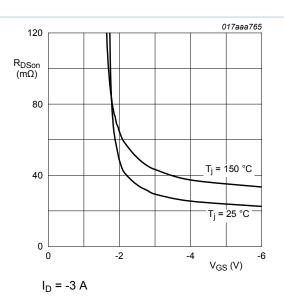


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

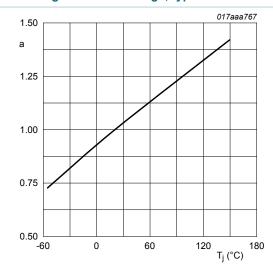


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$

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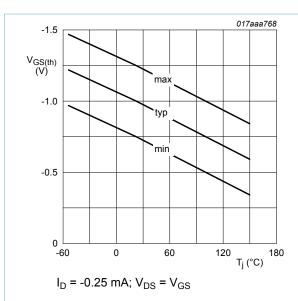


Fig. 12. Gate-source threshold voltage as a function of junction temperature

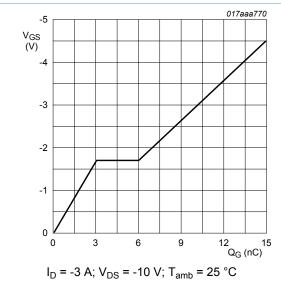


Fig. 14. Gate-source voltage as a function of gate charge; typical values

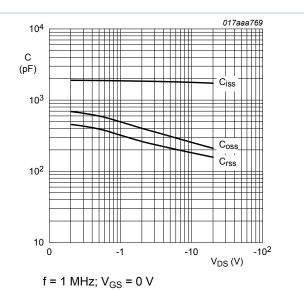


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

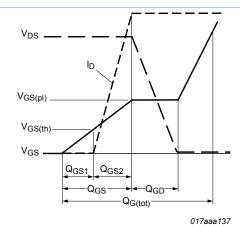
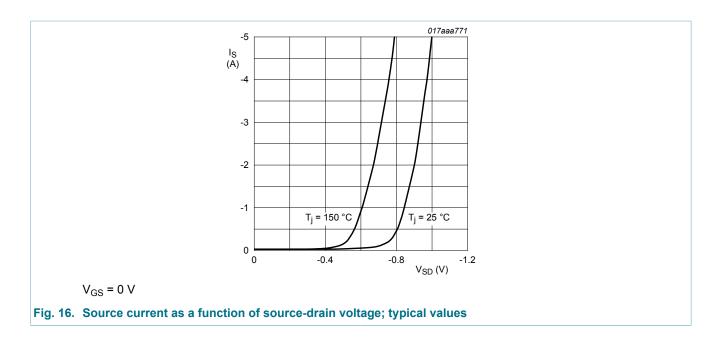
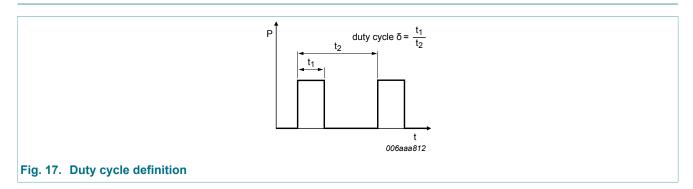


Fig. 15. Gate charge waveform definitions

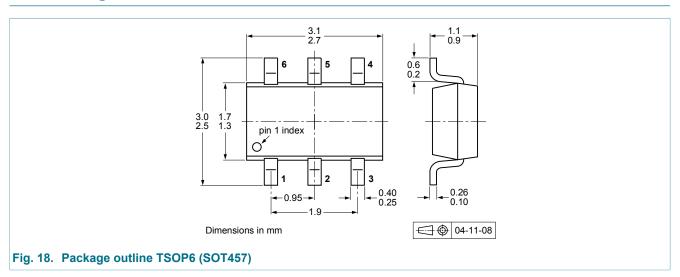
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8. Test information



9. Package outline

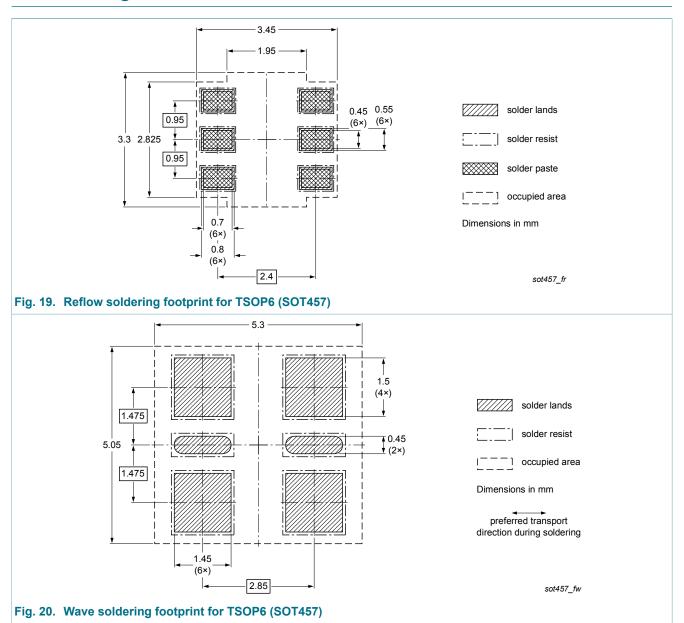


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10. Soldering



11. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMN27XPE v.1	20120920	Product data sheet	-	-

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Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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