

N-channel 80 V, 4.3 mΩ standard level MOSFET in I2PAK Rev. 02 — 18 April 2011 Product data

Product data sheet

Product profile 1.

1.1 General description

Standard level N-channel MOSFET in I2PAK package qualified to 175C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

1.2 Features and benefits

- High efficiency due to low switching and conduction losses
- Suitable for standard level gate drive

1.3 Applications

- DC-to-DC converters
- Load switch

- Motor control
- Server power supplies

1.4 Quick reference data

Table 1. **Quick reference data**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	80	V
I _D	drain current	$T_{mb} = 25 \text{ °C}; V_{GS} = 10 \text{ V};$ see <u>Figure 1</u>	<u>[1]</u>	-	-	120	A
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	-	306	W
Tj	junction temperature			-55	-	175	°C
Static cha	racteristics						
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; see <u>Figure 13</u>	[2]	-	3.7	4.3 7.1	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ T _j = 100 °C; see <u>Figure 12</u>	[2]	-	6.1		mΩ
Dynamic of	characteristics						
Q_{GD}	gate-drain charge	$V_{GS} = 10 \text{ V}; \text{ I}_{D} = 75 \text{ A};$		-	28	-	nC
Q _{G(tot)}	total gate charge	$V_{DS} = 40 V$; see <u>Figure 14</u> ; see <u>Figure 15</u>		-	111	-	nC
Avalanche	e ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$ \begin{array}{l} V_{GS} = 10 \; V; \; T_{j(init)} = 25 \; ^{\circ}C; \\ I_{D} = 120 \; A; \; V_{sup} \leq 80 \; V; \\ R_{GS} = 50 \; \Omega; \; unclamped \end{array} $		-	-	676	mJ



N-channel 80 V, 4.3 m Ω standard level MOSFET in I2PAK

- [1] Continuous current is limited by package.
- [2] Measured 3 mm from package.

2. Pinning information

Table 2.	Pinning	j information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		_
2	D	drain	mb	
3	S	source		
mb	D	drain		G
				mbb076 Ś
			$\overline{1}$ $\overline{2}$ $\overline{3}$	

SOT226 (I2PAK)

3. Ordering information

Table 3.Ordering information

Type number	Package		
	Name	Description	Version
PSMN4R3-80ES	I2PAK	plastic single-ended package (I2PAK); TO-262	SOT226

N-channel 80 V, 4.3 m Ω standard level MOSFET in I2PAK

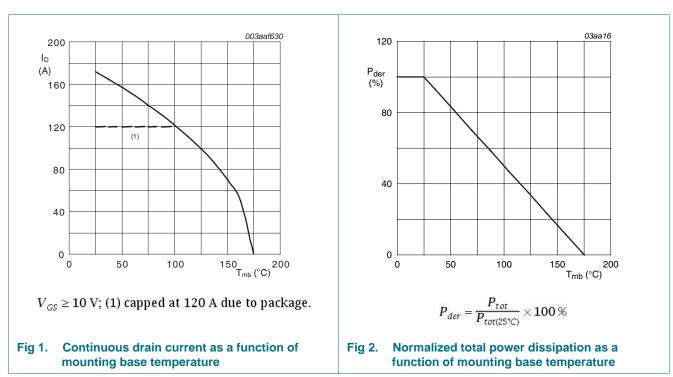
4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	80	V
V _{DGR}	drain-gate voltage	T _j ≥ 25 °C; T _j ≤ 175 °C; R _{GS} = 20 kΩ		-	80	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 100 °C; see <u>Figure 1</u>	[1]	-	120	А
		V _{GS} = 10 V; T _{mb} = 25 °C; see <u>Figure 1</u>	[1]	-	120	А
I _{DM}	peak drain current	pulsed; $t_p \le 10 \ \mu$ s; $T_{mb} = 25 \ ^{\circ}C$; see Figure 3		-	688	А
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	306	W
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
T _{sld(M)}	peak soldering temperature			-	260	°C
Source-drain	diode					
Is	source current	T _{mb} = 25 °C	[1]	-	120	А
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$		-	688	А
Avalanche ru	ggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 120 A; $V_{sup} \le 80$ V; R_{GS} = 50 Ω ; unclamped		-	676	mJ

[1] Continuous current is limited by package.



PSMN4R3-80ES

N-channel 80 V, 4.3 m Ω standard level MOSFET in I2PAK

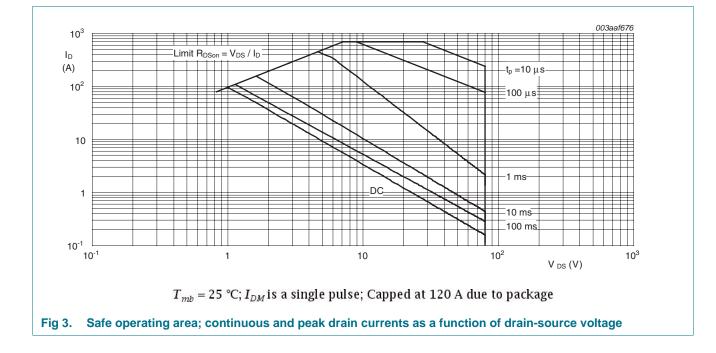


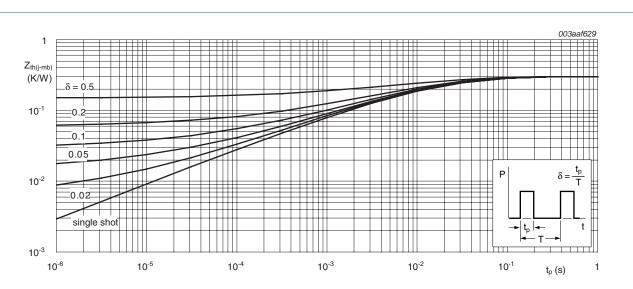
Table 5.

N-channel 80 V, 4.3 m Ω standard level MOSFET in I2PAK

Thermal characteristics 5.

Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	see Figure 4	-	0.22	0.49	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	Vertical in free air	-	60	-	K/W



Transient thermal impedance from junction to mounting base as a function of pulse duration Fig 4.

N-channel 80 V, 4.3 mΩ standard level MOSFET in I2PAK

6. Characteristics

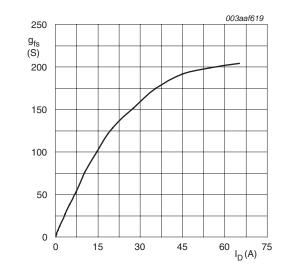
Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
V _{(BR)DSS}	drain-source breakdown	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = -55 \ ^\circ C$	73	-	-	V
	voltage	$I_D = 250 \ \mu\text{A}; \ V_{GS} = 0 \ V; \ T_j = 25 \ ^\circ\text{C}$	80	-	-	V
V _{GS(th)}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ see <u>Figure 10</u>	1	-	-	V
		I _D = 1 mA; V _{DS} = V _{GS} ; T _j = -55 °C; see <u>Figure 10</u>	-	-	4.6	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ see <u>Figure 10</u> ; see <u>Figure 11</u>	2	3	4	V
I _{DSS}	drain leakage current	V_{DS} = 80 V; V_{GS} = 0 V; T_j = 25 °C	-	0.02	10	μA
		$V_{DS} = 80 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$	-	-	500	μA
I _{GSS}	gate leakage current	V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
		$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 175 °C; see <u>Figure 12</u>	<u>[1]</u>	8.9	10.3	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; see <u>Figure 13</u>	<u>[1]</u> -	3.7	4.3	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 100 \text{ °C};$ see <u>Figure 12</u>	<u>[1]</u> -	6.1	7.1	mΩ
R _G	internal gate resistance (AC)	f = 1 MHz	-	0.9	-	Ω
Dynamic	characteristics					
Q _{G(tot)}	total gate charge	$I_D = 0 \text{ A}; V_{DS} = 0 \text{ V}; V_{GS} = 10 \text{ V}$	-	104	-	nC
		$I_D = 75 \text{ A}; V_{DS} = 40 \text{ V}; V_{GS} = 10 \text{ V};$	-	111	-	nC
Q_{GS}	gate-source charge	see Figure 14; see Figure 15	-	38	-	nC
Q _{GS(th)}	pre-threshold gate-source charge		-	24	-	nC
Q _{GS(th-pl)}	post-threshold gate-source charge		-	14	-	nC
Q _{GD}	gate-drain charge		-	28	-	nC
V _{GS(pl)}	gate-source plateau voltage	$I_D = 25 \text{ A}; V_{DS} = 40 \text{ V};$ see <u>Figure 14</u> ; see <u>Figure 15</u>	-	6.1	-	V
C _{iss}	input capacitance	$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$	-	8161	-	pF
C _{oss}	output capacitance	T _j = 25 °C; see <u>Figure 16</u>	-	701	-	pF
C _{rss}	reverse transfer capacitance		-	337	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 40 \text{ V}; \text{ R}_{L} = 0.53 \Omega;$	-	38	-	ns
t _r	rise time	$V_{GS} = 10 \text{ V}; \text{ R}_{G(ext)} = 4.7 \Omega;$	-	29	-	ns
t _{d(off)}	turn-off delay time	– I _D = 75 A	-	94	-	ns
t _f	fall time		-	33	-	ns

PSMN4R3-80ES

N-channel 80 V, 4.3 m Ω standard level MOSFET in I2PAK

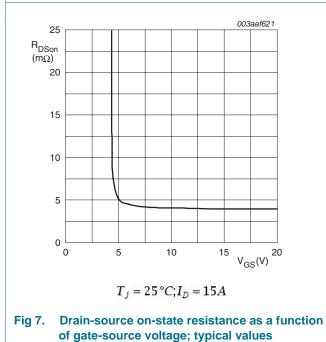
Table 6.	Characteristics continued					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Source-d	Irain diode					
V_{SD}	source-drain voltage	I _S = 25 A; V _{GS} = 0 V; T _j = 25 °C; see <u>Figure 17</u>	-	0.8	1.2	V
t _{rr}	reverse recovery time	$I_{S} = 25 \text{ A}; \text{ dI}_{S}/\text{dt} = 100 \text{ A}/\mu\text{s};$	-	59	-	ns
Qr	recovered charge	$V_{GS} = 0 V; V_{DS} = 20 V$	-	109	-	nC

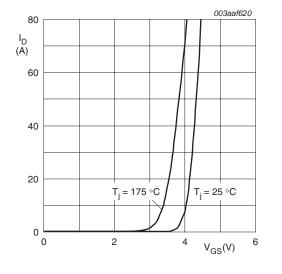
[1] Measured 3 mm from package.



 $T_j = 25 \,^{\circ}C; V_{DS} = 25V$

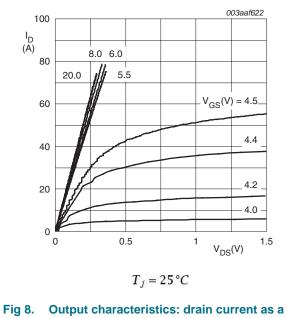








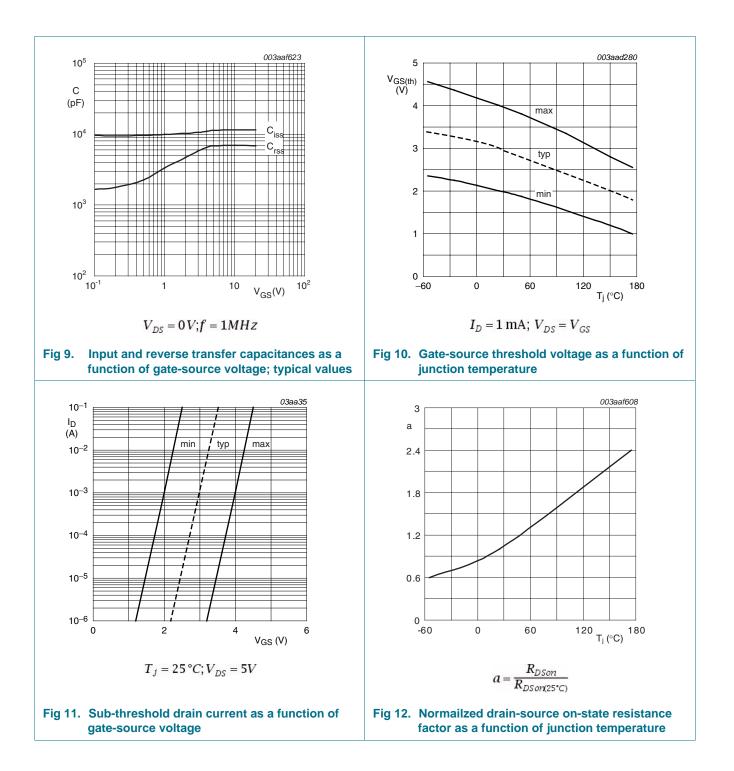






PSMN4R3-80ES

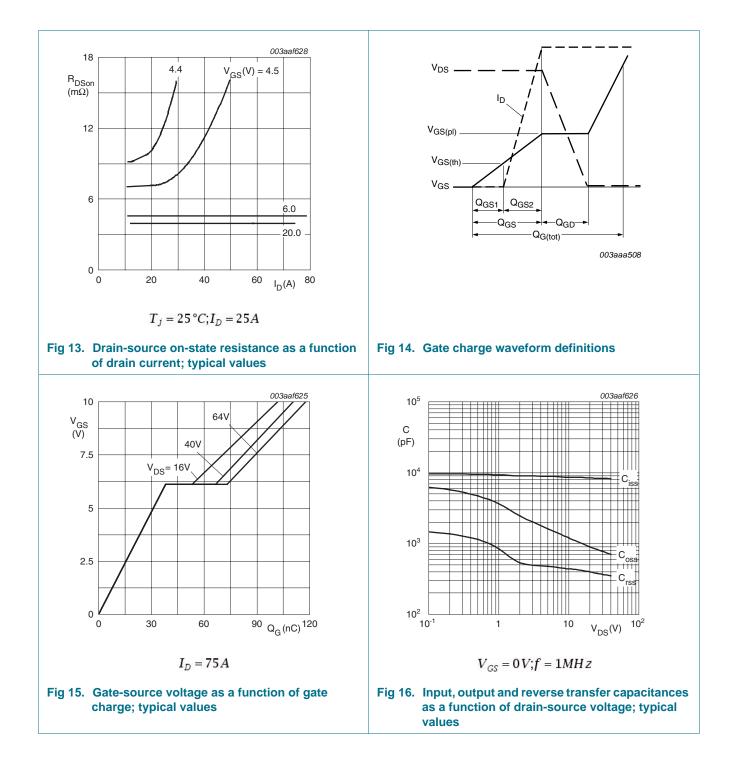
N-channel 80 V, 4.3 m Ω standard level MOSFET in I2PAK



8 of 15

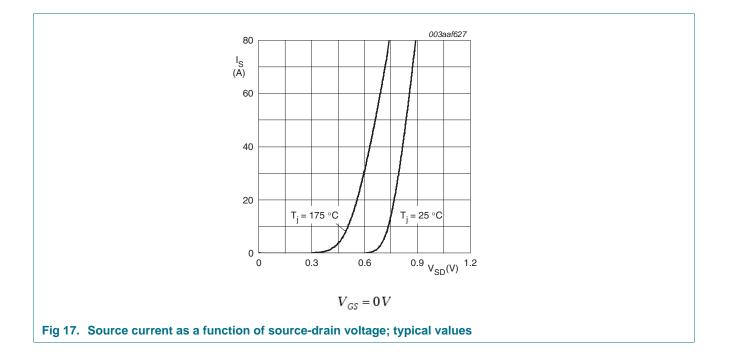
PSMN4R3-80ES

N-channel 80 V, 4.3 m Ω standard level MOSFET in I2PAK



PSMN4R3-80ES

N-channel 80 V, 4.3 mΩ standard level MOSFET in I2PAK



PSMN4R3-80ES

N-channel 80 V, 4.3 mΩ standard level MOSFET in I2PAK

7. Package outline

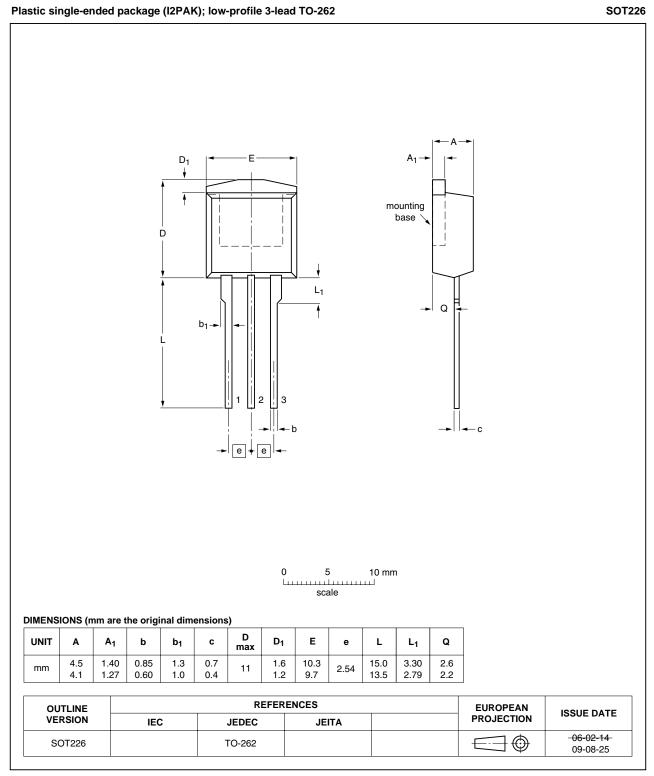


Fig 18. Package outline SOT226 (I2PAK)

All information provided in this document is subject to legal disclaimers.

N-channel 80 V, 4.3 mΩ standard level MOSFET in I2PAK

8. Revision history

Table 7. Revision h	nistory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PSMN4R3-80ES v.2	20110418	Product data sheet	-	PSMN4R3-80ES v.1
Modifications:	 Status change 	d from objective to product.		
	 Various chang 	es to content.		
PSMN4R3-80ES v.1	20101228	Objective data sheet	-	-

9. Legal information

9.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.nxp.com</u>.

9.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

9.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product sole and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective

N-channel 80 V, 4.3 m Ω standard level MOSFET in I2PAK

agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the

10. Contact information

For more information, please visit: <u>http://www.nxp.com</u>

For sales office addresses, please send an email to: salesaddresses@nxp.com

product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

9.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Adelante, Bitport, Bitsound, CoolFlux, CoReUse, DESFire, EZ-HV, FabKey, GreenChip, HiPerSmart, HITAG, I²C-bus logo, ICODE, I-CODE, ITEC, Labelution, MIFARE, MIFARE Plus, MIFARE Ultralight, MoReUse, QLPAK, Silicon Tuner, SiliconMAX, SmartXA, STARplug, TOPFET, TrenchMOS, TriMedia and UCODE — are trademarks of NXP B.V.

HD Radio and **HD Radio** logo — are trademarks of iBiquity Digital Corporation.

N-channel 80 V, 4.3 m Ω standard level MOSFET in I2PAK

11. Contents

1	Product profile1
1.1	General description1
1.2	Features and benefits1
1.3	Applications1
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Limiting values3
5	Thermal characteristics5
6	Characteristics6
7	Package outline11
8	Revision history12
9	Legal information13
9.1	Data sheet status13
9.2	Definitions13
9.3	Disclaimers
9.4	Trademarks14
10	Contact information14

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2011.

All rights reserved.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 18 April 2011 Document identifier: PSMN4R3-80ES

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

NXP: PSMN4R3-80ES,127