VEC2415

Power MOSFET 60V, 80mΩ, 3A, Dual N-Channel

This Power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

Features

- Low On-Resistance
- 4V drive
- Low-Profile Package
- ESD Diode-Protected Gate
- Pb-Free and RoHS compliance
- Halogen Free compliance : VEC2415-TL-W

Typical Applications

- Motor Driver
- DC/DC Converter

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	60	V
Gate to Source Voltage	VGSS	±20	V
Drain Current (DC)	ID	3	А
Drain Current (Pulse) PW $\leq 10\mu$ s, duty cycle $\leq 1\%$	IDP	12	A
Power Dissipation When mounted on ceramic substrate ($900mm^2 \times 0.8mm$) 1unit	PD	0.9	W
Total Dissipation When mounted on ceramic substrate ($900mm^2 \times 0.8mm$)	PT	1.0	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	-55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (900mm ² \times 0.8mm) 1unit	$R_{\theta JA}$	138.8	°C/W

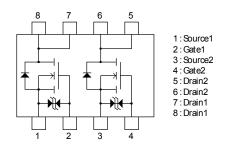


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VDSS	R _{DS} (on) Max	ID Max		
	80mΩ@ 10V			
60V	106mΩ@ 4.5V	3A		
	116mΩ@ 4V			

ELECTRICAL CONNECTION N-Channel



PACKING TYPE : TL MARKING



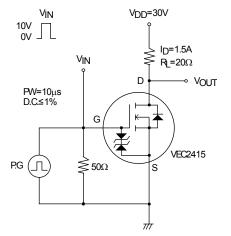
See detailed ordering and shipping information on page 5 of this data sheet.

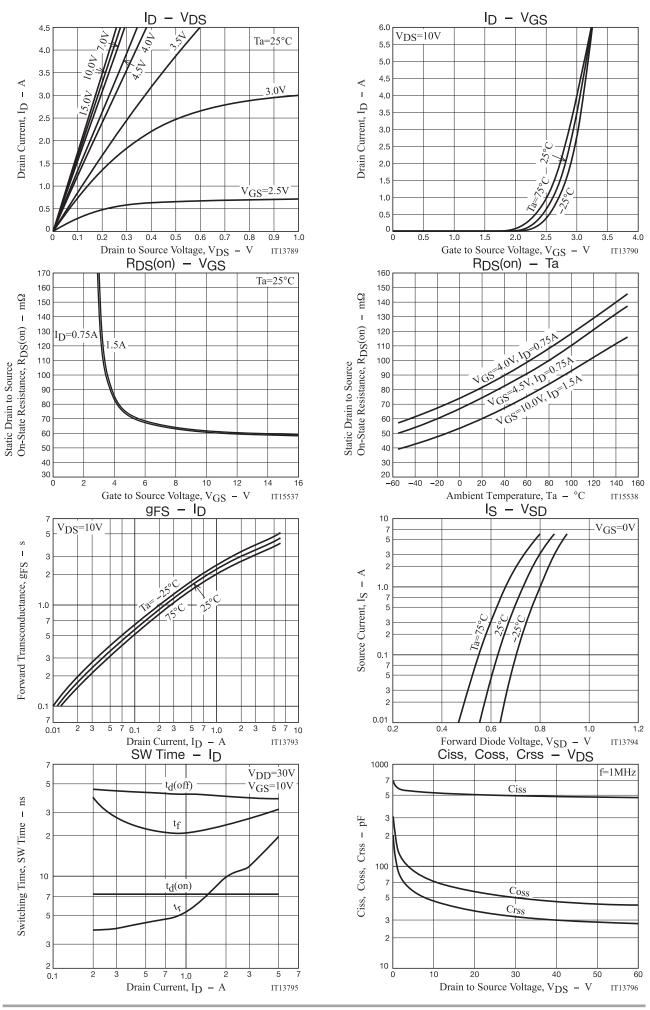
Parameter	Cumbol	Conditions		Value		
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	60			V
Zero-Gate Voltage Drain Current	IDSS	VDS=60V, VGS=0V			1	μA
Gate to Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μA
Gate Threshold Voltage	VGS(th)	V _{DS} =10V, I _D =1mA	1.2		2.6	V
Forward Transconductance	9FS	V _{DS} =10V, I _D =1.5A		2.6		S
	R _{DS} (on)1	ID=1.5A, VGS=10V		62	80	mΩ
Static Drain to Source On-State	R _{DS} (on)2	ID=0.75A, VGS=4.5V		76	106	mΩ
Resistance	R _{DS} (on)3	ID=0.75A, VGS=4V		83	116	mΩ
Input Capacitance	Ciss			505		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		57		pF
Reverse Transfer Capacitance	Crss			37		pF
Turn-ON Delay Time	t _d (on)			7.3		ns
Rise Time	tr			7.5		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		41		ns
Fall Time	tf			22		ns
Total Gate Charge	Qg			10		nC
Gate to Source Charge	Qgs	V _{DS} =30V, V _{GS} =10V, I _D =3A		1.6		nC
Gate to Drain "Miller" Charge	Qgd			2.1		nC
Forward Diode Voltage	V _{SD}	IS=3A, VGS=0V		0.81	1.2	V

ELECTRICAL CHARACTERISTICS at $Ta = 25^{\circ}C$ (Note 2)

Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

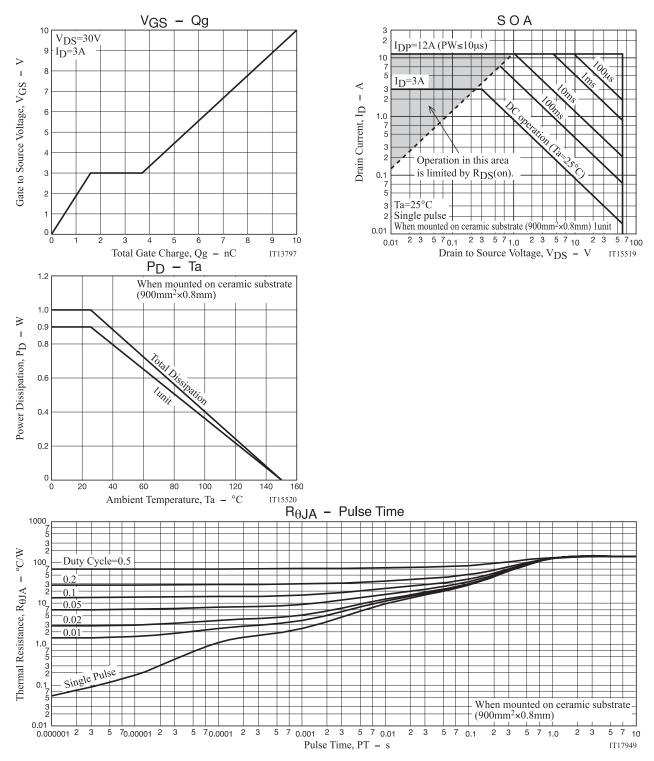
Switching Time Test Circuit





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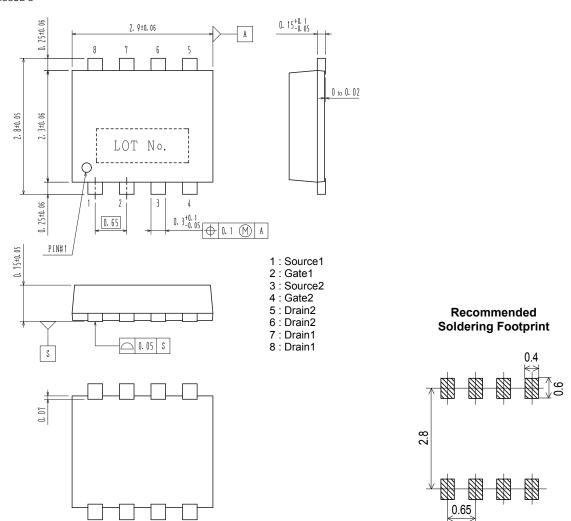
VEC2415



PACKAGE DIMENSIONS

unit : mm

SOT-28FL / VEC8 CASE 318AH ISSUE O



ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
VEC2415-TL-E		SOT-28FL / VEC8 (Pb-Free)	2.000 / Tana & Daal
VEC2415-TL-W	SOT-28FL / VEC8 (Pb-Free / Halogen Free)	3,000 / Tape & Reel	

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage : Since the VEC2415 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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