

# General purpose transistors(dual transistors)

## EMT18 / UMT18N / IMT18

#### Features

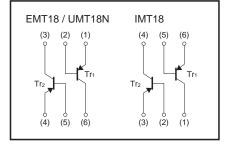
- 1) Two 2SA2018 chips in a EMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.

#### Structure

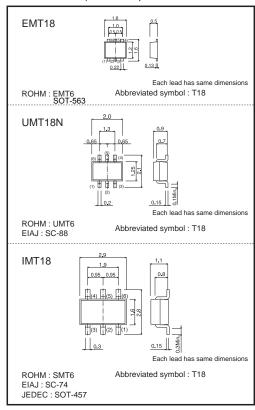
Epitaxial planar type PNP silicon transistor

The following characteristics apply to both Tr1 and Tr2.

#### Inner circuit



#### Dimensions (Unit : mm)



#### •Absolute maximum ratings (Ta=25°C)

Parameter	Sy	/mbol	Limits	Unit
Collector-base voltage	\	/сво	-15	V
Collector-emitter voltage	VCEO		-12	V
Emitter-base voltage	Vebo		-6	V
Collector current	lc		-500	mA
Collector current	ICP		1.0 *1	А
	Pc UMT6	150 (TOTAL) <sup>*2</sup>	2	
Power dissipation		UMT6	150 (TOTAL)	mW
		SMT6	300 (TOTAL)*3	
Junction temperature		Tj	150	°C
Storage temperature	1	Гstg	-55 to +150	°C

\*1 Single pulse Pw=1ms \*2 120mW per element must not be exceeded.

\*3 200mW per element must not be exceeded.

## •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВVсво	-15	-	-	V	Ic=-10μA	
Collector-emitter breakdown voltage	BVCEO	-12	_	-	V	Ic=-1mA	
Emitter-base breakdown voltage	ВУево	-6	-	-	V	Iε=-10μA	
Collector cutoff current	Ісво	-	-	-0.1	μΑ	V <sub>CB</sub> =-15V	
Emitter cutoff current	Іево	-	-	-0.1	μΑ	Vcb=-6V	
Collector-emitter saturation voltage	VCE (sat)	_	-100	-250	mV	Ic / I <sub>B</sub> =-200mA / -10mA	
DC current transfer ratio	hfe	270	-	680	-	Vce= -2V, Ic= -10mA	
Transition frequency	f⊤	-	260	-	MHz	V <sub>CE</sub> = -2V, I <sub>E</sub> =10mA, f=100MHz	
Output capacitance	Cob	_	6.5	_	pF	V <sub>CB</sub> =-10V, I <sub>E</sub> =0A, f=1MHz	

## ●Packaging specifications and hFE

	Package name		Taping	
Туре	Code	T2R	TR	T110
	Basic ordering unit (pieces)	8000	3000	3000
EMT18		0	-	-
UMT18N		-	0	-
IMT18		_	-	0

### •Electrical characteristic curves

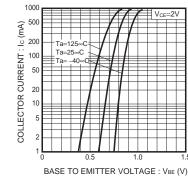
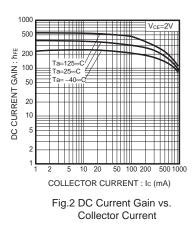
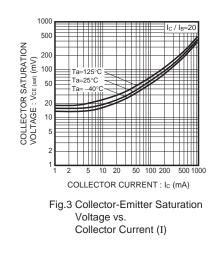


Fig.1 Grounded Emitter Propagation Characteristics





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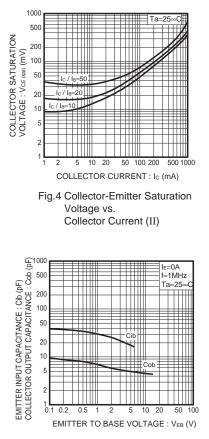
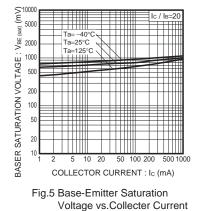


Fig.7 Collector Output Capacitance vs. Collector-Base Voltage Emitter Input Capacitance vs. Emitter-Base Voltage



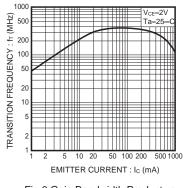


Fig.6 Gain Bandwidth Product vs. Emitter Current

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