General purpose transistors (dual transistors)

EMX18 / UMX18N

Features

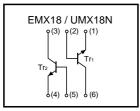
- 1) Two 2SC5585 chips in a EMT or UMT package.
- 2) Mounting possible with EMT3 or UMT3 automatic mounting machines.
- Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

●Structure

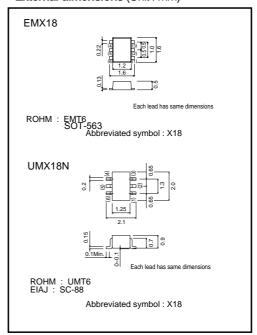
Epitaxial planar type NPN silicon transistor

The following characteristics apply to both Tr1 and Tr2.

●Equivalent circuit



●External dimensions (Unit: mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	15	V
Collector-emitter voltage	VCEO	12	V
Emitter-base voltage	V _{ЕВО}	6	V
Collector current	Ic	500	mA
	Іср	1.0	Α
Power dissipation	Pd	150 (TOTAL)	mW *1
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

^{*1 120}mW per element must not be exceeded.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	15	_	_	V	Ic=10μA
Collector-emitter breakdown voltage	BVceo	12	_	_	V	Ic=1mA
Emitter-base breakdown voltage	ВУево	6	_	_	V	Iε=10μA
Collector cutoff current	Ісво	_	_	0.1	μΑ	VcB=15V
Emitter cutoff current	ІЕВО	_	_	0.1	μΑ	V _{EB} =6V
Collector-emitter saturation voltage	VCE (sat)	_	90	250	mV	Ic/I _B =200mA/10mA
DC current transfer ratio	hfe	270	_	680	_	Vce=2V, Ic=10mA
Transition frequency	f⊤	_	320	_	MHz	Vc=2V, I==-10mA, f=100MHz
Output capacitance	Cob	_	7.5	_	PF	Vcb=10V, Ie=0A, f=1MHz

Packaging specifications

	Package	Taping	
	Code	T2R	TN
Туре	Basic ordering unit (pieces)	8000	3000
EMX18		0	_
UMX18N		_	0

•Electrical characteristic curves

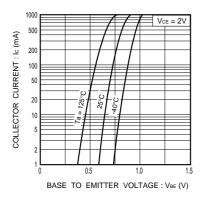


Fig.1 Grounded emitter propagation characteristics

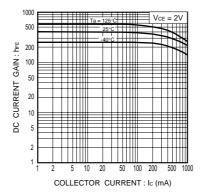


Fig.2 DC current gain vs. collector current

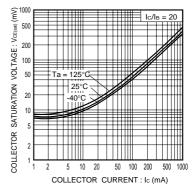


Fig.3 Collector-emitter saturation voltage vs. collector current (I)

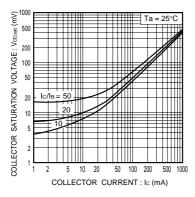


Fig.4 Collector-emitter saturation voltage vs. collector current (II)

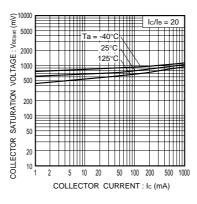


Fig.5 Base-emitter saturation voltage vs. collector current

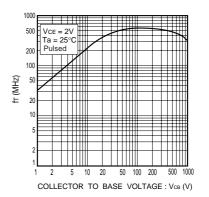


Fig.6 Collector output capacitance Emitter input capacitance vs. base voltage

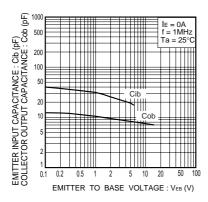


Fig.7 Collector output capacitance vs collector-base voltage Emitter input capacitance vs emitter-base voltage

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