2.5V Drive Nch+Nch MOS FET

QS6K1

Structure

Silicon N-channel MOS FET

●Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small and Surface Mount Package (TSMT6).

Application

Power switching, DC / DC converter.

Packaging specifications

	Package	Taping
Type	Code	TR
	Basic ordering unit (pieces)	3000
QS6K1		0

● Absolute maximum ratings (Ta=25°C)

<It is the same ratings for the Tr1 and Tr2>

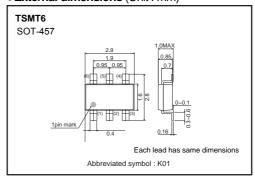
Parameter		Symbol	Limits	Unit	
Drain-source voltage		V _{DSS}	30	V	
Gate-source voltage		V _{GSS}	12	V	
Drain current	Continuous	ID	±1.0	Α	
	Pulsed	I _{DP} *1	±4.0	Α	
Source current	Continuous	Is	0.8	Α	
(Body diode)	Pulsed	I _{SP} *1	4.0	Α	
Total power dissipation (Tc=25°C)		Pp *2	1.25	W / TOTAL	
		PD ~	0.9	W / ELEMENT	
Channel temperature		Tch	150	°C	
Storage temperature		Tstg	-55 to +150	°C	
*1 Duy/10ug Duty avalo/19/					

●Thermal resistance

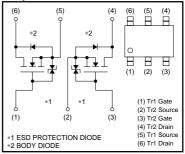
Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-a)*	100	°C / W / TOTAL
Chariner to ambient	Kill (Cli-a)	139	°C / W / ELEMENT

^{*} Mounted on a ceramic board

●External dimensions (Unit: mm)



Equivalent circuit



A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

●Electrical characteristics (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	10	μΑ	Vgs=12V, Vps=0V
Drain-source breakdown voltage	V _{(BR) DSS}	30	_	_	٧	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	-	_	1	μΑ	V _{DS} =30V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	0.5	_	1.5	٧	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance		_	170	238	mΩ	I _D =1.0A, V _{GS} =4.5V
	R _{DS (on)} *	-	180	252		I _D =1.0A, V _{GS} =4.0V
		-	260	364		I _D =1.0A, V _{GS} =2.5V
Forward transfer admittance	Y _{fs} *	1.0	_	_	S	I _D =1.0A, V _{DS} =10V
Input capacitance	Ciss	-	77	_	pF	V _{DS} =10V
Output capacitance	Coss	_	25	_	рF	Vgs=0V
Reverse transfer capacitance	Crss	_	15	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	_	7	_	ns	I _D =500mA, V _{DD} ≒15V
Rise time	tr *	_	7	_	ns	V _{GS} =4.5V
Turn-off delay time	t _{d (off)} *	_	15	_	ns	R _L =30.0Ω
Fall time	t _f *	-	6	_	ns	R _G =10Ω
Total gate charge	Qg *	_	1.7	2.4	nC	V _{DD} ≒15V
Gate-source charge	Q _{gs} *	_	0.4	_	nC	V _{GS} =4.5V
Gate-drain charge	Q _{gd} *	_	0.4	_	nC	I _D =1.0A

^{*}Pulsed

●Body diode characteristics (Source-Drain) (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp *	_	_	1.2	٧	Is=3.2A, Vgs=0V

^{*}Pulsed

Electrical characteristic curves

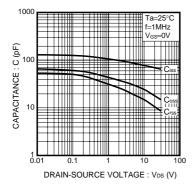


Fig.1 Typical Capacitance vs. Drain-Source Voltage

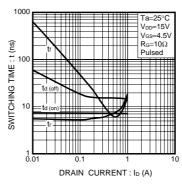


Fig.2 Switching Characteristics

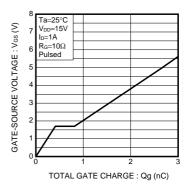


Fig.3 Dynamic Input Characteristics

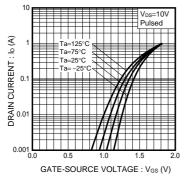


Fig.4 Typical Transfer Characteristics

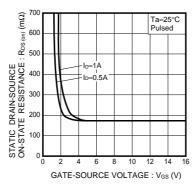


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

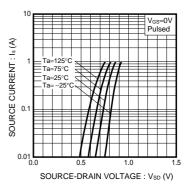


Fig.6 Source Current vs. Source-Drain Voltage

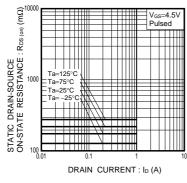


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

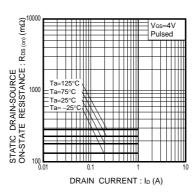


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

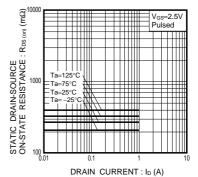


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

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