

4V Drive Pch MOSFET RT1E050RP

Structure

Silicon P-channel MOSFET

Features

1) Low on-resistance.

2) High power package.

3) 4V drive.

Application

Switching

Packaging specifications

Туре	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RT1E050R	P	0

• Absolute maximum ratings (Ta = 25°C)

		/		
Parameter		Symbol	Limits	Unit
Drain-source voltage	source voltage V _{DSS} -30 V		V	
Gate-source voltage		V _{GSS}	±20	V
Drain current	Continuous	Ι _D	±5	А
Drain current	Pulsed	I _{DP} *1	±20	А
Source current	Continuous	I _S	-1	А
(Body Diode)	Pulsed	ا _{SP} *1	-20	А
Power dissipation		P _D *2	1.25	W
Channel temperature	e	Tch	150	°C
Range of storage ter	f storage temperature Tstg -55 to +150		°C	

*1 Pw≤10μs, Duty cycle≤1%

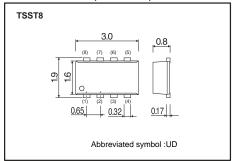
*2 Mounted on a ceramic board.

• Thermal resistance

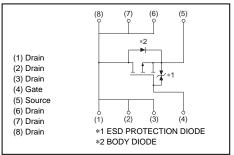
Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	100	°C / W

*Mounted on a ceramic board.

• **Dimensions** (Unit : mm)



• Inner circuit



Data Sheet

• Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±10	μA	V _{GS} =±20V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR)DSS}	-30	-	-	V	I _D =-1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	-	-	-1	μA	V_{DS} =-30V, V_{GS} =0V
Gate threshold voltage	V _{GS (th)}	-1.0	-	-2.5	V	V_{DS} =-10V, I_{D} =-1mA
	*	-	26	36		I _D =–5A, V _{GS} =–10V
Static drain-source on-state resistance	R _{DS (on)}	-	36	50	mΩ	I _D =-2.5A, V _{GS} =-4.5V
		-	40	56		I _D =-2.5A, V _{GS} =-4.0V
Forward transfer admittance	I Y _{fs} ľ	3.1	-	-	s	I _D =-5A, V _{DS} =-10V
Input capacitance	C _{iss}	-	1300	-	pF	V _{DS} =-10V
Output capacitance	C _{oss}	-	180	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	-	160	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	-	10	-	ns	I _D =–2.5A, V _{DD} ≒–15V
Rise time	t _r *	-	15	-	ns	V _{GS} =-10V
Turn-off delay time	$t_{d(off)}^{*}$	-	90	-	ns	R _L =6.0Ω
Fall time	t _f *	-	50	-	ns	R _G =10Ω
Total gate charge	Q _g *	-	13	-	nC	I _D =–5A, V _{DD} ≒−15V
Gate-source charge	Q _{gs} *	-	3.5	-	nC	V_{GS} =-5V R _L =3 Ω
Gate-drain charge	Q _{gd} *	-	4.5	-	nC	R _G =10Ω

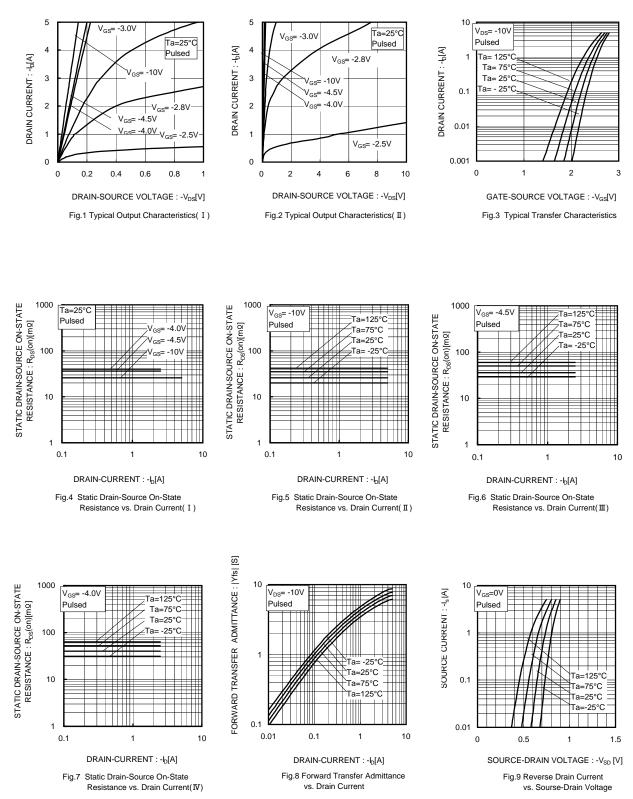
*Pulsed

•Body diode characteristics (Source-Drain) (Ta = 25°C)

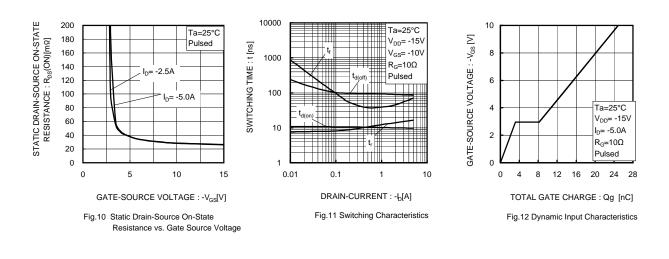
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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V_{SD}^{*}	-	-	-1.2	V	I_s =-5A, V_{GS} =0V

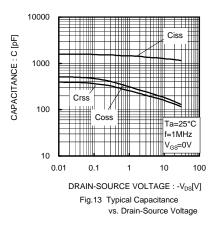
*Pulsed

• Electrical characteristics curves



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• Measurement circuits

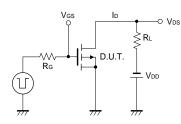
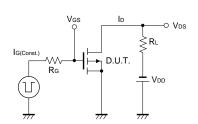


Fig.1-1 Switching Time Measurement Circuit



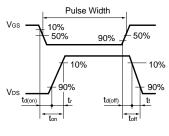


Fig.1-2 Switching Waveforms

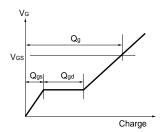


Fig.2-1 Gate Charge Measurement Circuit

Fig.2-2 Gate Charge Waveform

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