DC-DC Converter (–20V, –1.5A) RTF015P02

Features

- 1) Low on-resistance. ($80m\Omega$ at 2.5V)
- 2) High power package.
- 3) High speed switching.
- 4) Low voltage drive. (2.5V)

Applications

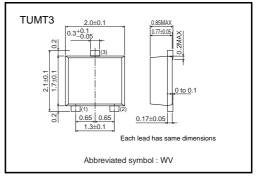
Structure

MOS FET

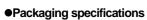
Silicon P-channel

DC-DC converter

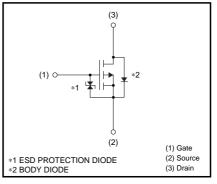
•External dimensions (Unit : mm)



Equivalent circuit



Туре	Package	Taping	
	Code	TR	
	Basic ordering unit (pieces)	3000	
RTF015P02		0	





Transistors

•Absolute maximum ratings (Ta=25°C)

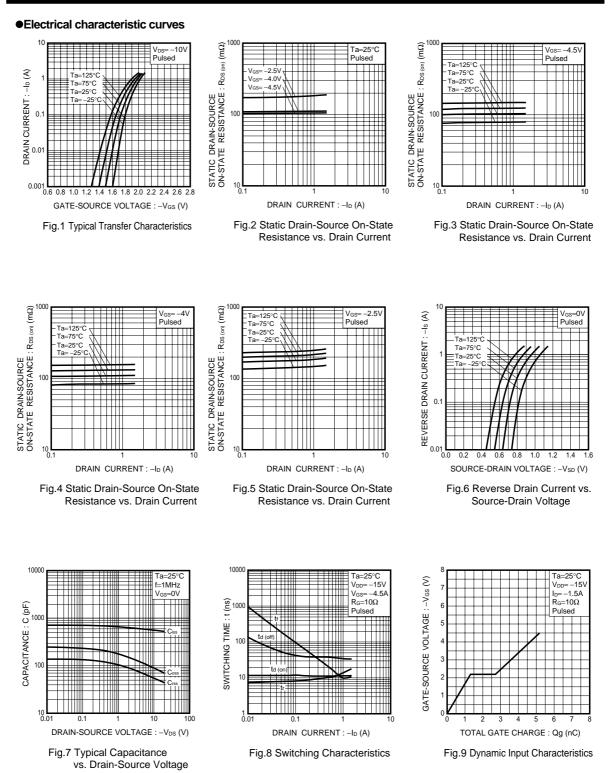
Parameter Drain-source voltage		Symbol	Limits	Unit			
		VDSS	-20	V V			
Gate-source voltage		Vgss	±12				
Drain current	Continuous	lo	±1.5	А			
	Pulsed	DP *1	±6	А			
Source current (Body diode)	Continuous	ls *1	-0.6	A			
	Pulsed	Isp	-6	А			
Total power dissipation		Pd *2	0.8	W			
Channel temperature		Tch	150	°C			
Range of Storage temperature		Tstg	-55 to +150	°C			

*1 Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	-	-	±10	μΑ	V _{GS} =±12V, V _{DS} =0V	
Drain-source breakdown voltage	$V_{(BR)DSS}$	-20	-	_	V	$I_D = -1mA$, $V_{GS} = 0V$	
Zero gate voltage drain current	IDSS	-	-	-1	μA	VDS=-20V, VGS=0V	
Gate threshold voltage	VGS (th)	-0.7	-	-2.0	V	$V_{DS} = -10V$, $I_{D} = -1mA$	
Static drain-source on-state resistance	R _{DS} (on)	-	100	135	mΩ	$I_D = -1.5A$, $V_{GS} = -4.5V$	
		_	110	150	mΩ	I_D = -1.5A, V_{GS} = -4V	
		_	180	250	mΩ	$I_D = -1.5A$, $V_{GS} = -2.5V$	
Forward transfer admittance	Y _{fs} *	1.5	_	_	S	V_{DS} = -10V, I_{D} = -0.8A	
Input capacitance	Ciss	-	560	_	pF	$V_{DS} = -10V$	
Output capacitance	Coss	-	90	_	pF	V _{GS} =0V	
Reverse transfer capacitance	Crss	-	55	_	pF	f=1MHz	
Turn-on delay time	td (on) $*$	-	12	_	ns	$\begin{array}{l} \text{Ib}=-0.8\text{A}\\ \text{V}_{\text{DD}}\coloneqq-15\text{V}\\ \text{V}_{\text{GS}}=-4.5\text{V}\\ \text{R}_{\text{L}}=9\Omega\\ \text{R}_{\text{GS}}=10\Omega \end{array}$	
Rise time	tr *	-	12	_	ns		
Turn-off delay time	t _{d (off)} *	-	38	_	ns		
Fall time	t _f *	_	12	_	ns		
Total gate charge	Qg	_	5.2	_	nC	V _{DD} ≒−15V RL≒10Ω	
Gate-source charge	Qgs	-	1.3	_	nC	$V_{GS}=-4.5V$ R _{GS} =10 Ω	
Gate-drain charge	Q _{gd}	-	1.4	-	nC	I _D =-1.5A	
*Pulsed							
Body diode characteristics (source-drain characteristics)							
Forward voltage	VSD	-	_	-1.2	V	I _S = -0.6A, V _{GS} =0V	

Transistors



Transistors

Measurement circuits

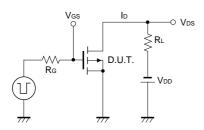


Fig.10 Switching Time Measurement Circuit

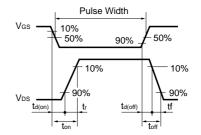


Fig.11 Switching Waveforms

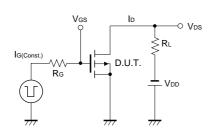


Fig.12 Gate Charge Measurement Circuit

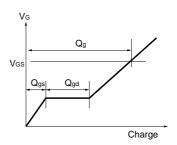


Fig.13 Gate Charge Waveforms

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