Pch -20V -1.4A Small Signal MOSFET

V <sub>DSS</sub>	-20V
R <sub>DS(on)</sub> (Max.)	300mΩ
I <sub>D</sub>	±1.4A
$P_D$	600mW

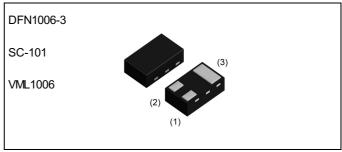
# ● Features

- 1) Low on Resistance, High Current.
- 2) High Power small mold Package (DFN1006).
- 3) Pb-free lead plating; RoHS compliant.
- 4) Halogen Free.

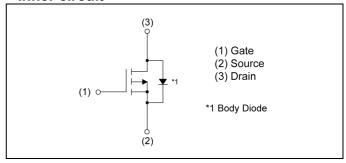
## Application

Switching

### Outline



## Inner circuit



Packaging specifications

- i destagning e premiedatione				
	Packing	Embossed Tape		
	Reel size (mm)	180		
Туре	Tape width (mm)	8		
	Basic ordering unit (pcs)	8000		
	Taping code	T2CL		
	Marking	SZ		

## ullet Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V <sub>DSS</sub>	-20	V	
	V <sub>GS</sub> = -4.5V	I <sub>D</sub> *2	±1.4	Α
Continuous drain current	V <sub>GS</sub> = -2.5V	I <sub>D</sub> *2	±1.2	Α
	V <sub>GS</sub> = -1.8V	I <sub>D</sub> *2	±0.9	Α
		I <sub>D</sub> *3	±0.7	Α
Pulsed drain current	I <sub>DP</sub> *1	±2.8	Α	
Gate - Source voltage	$V_{GSS}$	±8	V	
Power dissipation	$P_{D}^{*2}$	600	mW	
rowei dissipation		P <sub>D</sub> *3	400	mW
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage tem	perature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Doromotor	Symbol	Values			l limit
Parameter		Min.	Тур.	Max.	Unit
Thermal registance innetion, ambient	R <sub>thJA</sub> *2	-	-	208	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *3	-	-	312	°C/W

## ● Electrical characteristics (T<sub>a</sub> = 25°C)

Daramatar	Symbol		Values			1.1:4
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = -1mA$		-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j} I_D = -1 \text{mA}$ referenced to 25°C		-10.3	-	mV/°C
Zero gate voltage drain current	I <sub>DSS</sub>	$I_{DSS}$ $V_{DS} = -20V$ , $V_{GS} = 0V$		-	-1	μΑ
Gate - Source leakage current	I <sub>GSS</sub>	$I_{GSS}$ $V_{GS} = \pm 8V, V_{DS} = 0V$		-	±100	nA
Gate threshold voltage	$V_{GS(th)} V_{DS} = V_{GS}, I_{D} = -100 \mu A$		-0.3	-	-1.0	V
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = -100μA referenced to 25°C	-	1.7	-	mV/°C
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -1.4A	-	220	300	
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -1.2A	-	280	390	mΩ
on state registaries		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -0.9A	-	370	700	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	$V_{DS} = -5V, I_{D} = -0.1A$	0.3	-	-	S

<sup>\*1</sup> Pw≦10µs , Duty cycle≦1%

<sup>\*2</sup> Mounted on a FR4 Board (25.4mm x 25.4mm x 0.8mm, Cu Pad : 645mm2), Pw≦5s

<sup>\*3</sup> Mounted on a FR4 Board (20.0mm x 12.0mm x 0.8mm, Cu Pad : 45mm2)

<sup>\*4</sup> Pulsed

## ●Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumphal	Conditions	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	100	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	19	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	17	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq -10V, V_{GS} = -4.5V$	-	3.1	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -0.7A	-	12.0	-	no
Turn - off delay time	t <sub>d(off)</sub> *4	R <sub>L</sub> ≃ 14.3Ω	-	23.0	-	ns
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	3.1	-	

## •Body diode electrical characteristics (Source-Drain) ( $T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offit
Continuous forward current	I <sub>S</sub> *2	T = 25°C	-	-	-0.5	Α
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	-2.8	Α
Forward voltage	V <sub>SD</sub> *4	$V_{GS} = 0V, I_{S} = -0.5A$	-	-	-1.2	V

Fig.1 Power Dissipation Derating Curve

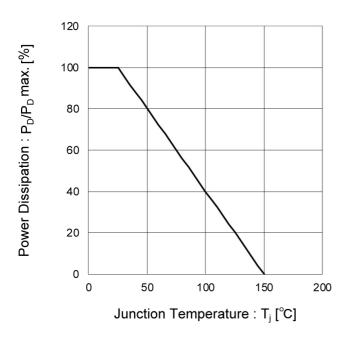
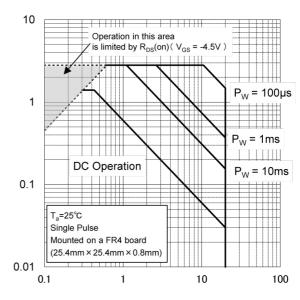


Fig.2 Maximum Safe Operating Area



Drain Current: -l<sub>D</sub> [A]

Drain - Source Voltage : -V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

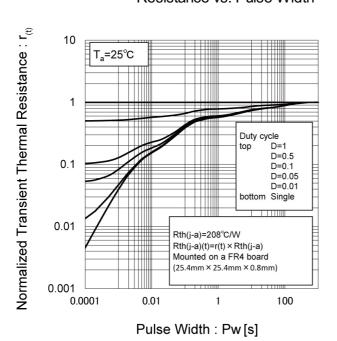
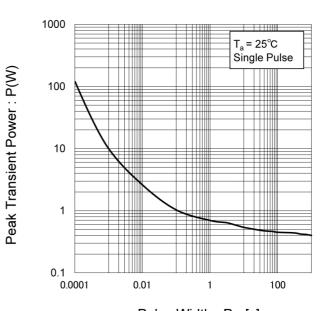


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width : Pw [s]

Drain Current : -I<sub>D</sub> [A]

#### • Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

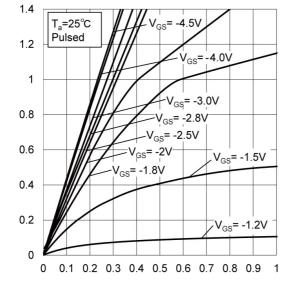
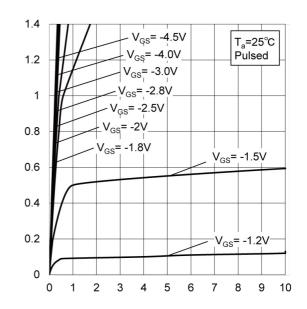


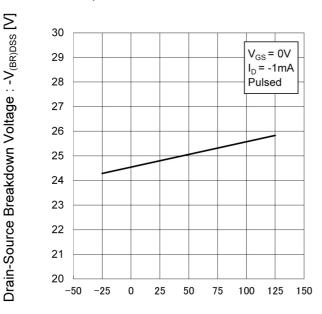
Fig.6 Typical Output Characteristics(II)



Drain Current : -I<sub>D</sub> [A]

Drain - Source Voltage : -V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs. Junction Temperature



Junction Temperature : T<sub>i</sub> [°C]

Fig.8 Typical Transfer Characteristics

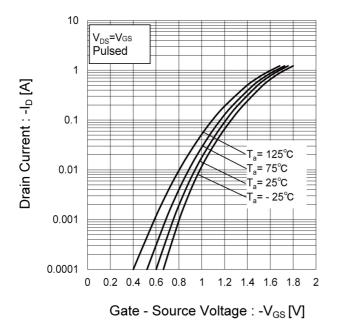


Fig.9 Gate Threshold Voltage vs. Junction Temperature

Gate Threshold Voltage : - $V_{\text{GS(th)}}$  [V]

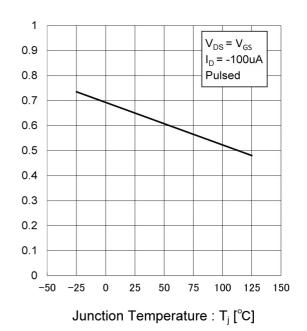


Fig.10 Forward Transfer Admittance vs. Drain Current

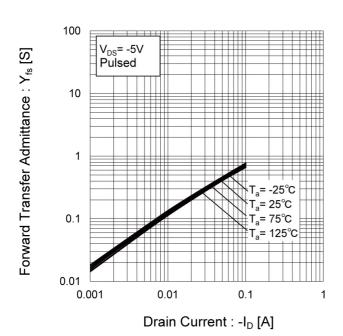


Fig.11 Drain Current Derating Curve

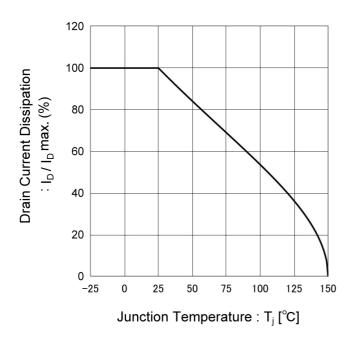
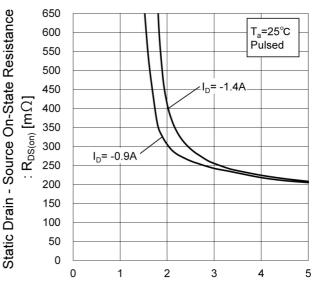


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage :  $-V_{GS}[V]$ 

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

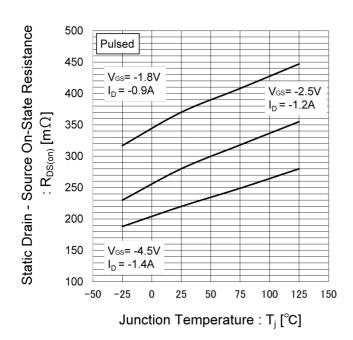


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

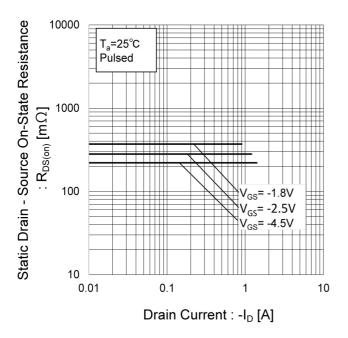


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

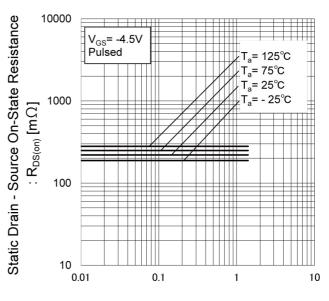


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

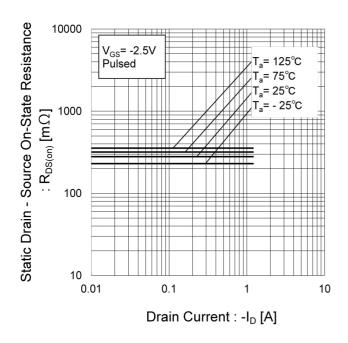


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

Drain Current: -ID [A]

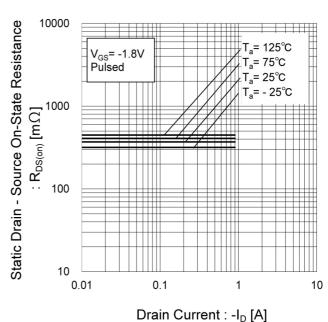


Fig.18 Typical Capacitance vs. Drain - Source Voltage

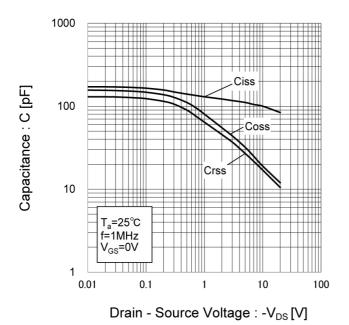


Fig.19 Switching Characteristics

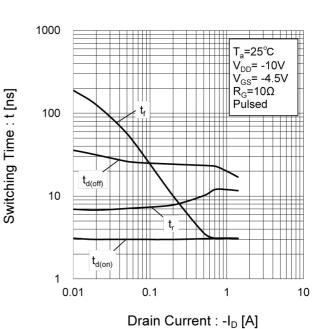
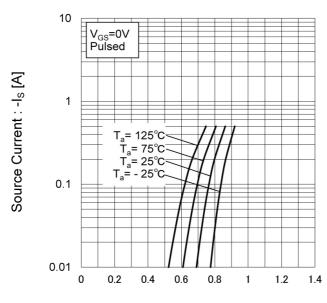


Fig.20 Source Current vs. Source Drain Voltage



Source - Drain Voltage : -V<sub>SD</sub> [V]

## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

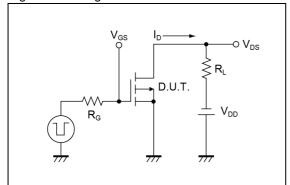
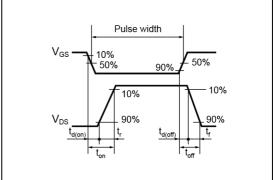


Fig.1-2 Switching Waveforms



## Notice

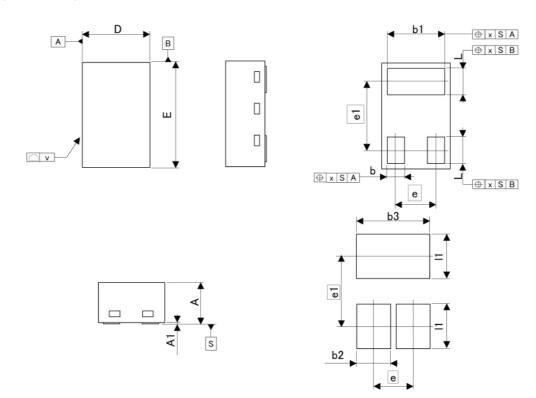
1. This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

## 2.Reference data of Vesd

Parameter	Symbol	Condition	Тур.	Unit
Electrostatic discharge voltage	V <sub>esd</sub>	C=100pF,R=1.5kΩ (Human body model)	200	V

## Dimensions

DFN1006-3 (VML1006)



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	TERS	INCI	HES	
DIIVI	MIN	MAX	MIN	MAX	
Α	0.34	0.40	0.013	0.016	
A1	0.00	0.05	0.000	0.002	
b	0.10	0.20	0.004	0.008	
b1	0.45	0.55	0.018	0.022	
D	0.55	0.65	0.022	0.026	
Е	0.95	1.05	0.037	0.041	
е	0.	35	0.014		
e1	0.	65	0.0	)26	
L	0.20	0.30	0.008	0.012	
Х	2	0.10	ì	0.004	
V	-	0.05	-	0.002	

DIM	MILIME	MILIMETERS		HES	
	DIIVI	MIN	MAX	MIN	MAX
	b2	-	0.3	-	0.012
	b3	_	0.65		0.026
	I1	-	0.40	-	0.016

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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  - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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