SCS220AE2

SiC Schottky Barrier Diode

V_R	650V		
l _F	10A/20A*		
Q_{C}	15nC		

*(Per leg / Both legs)

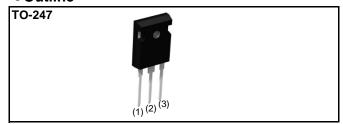
Features

- 1) Shorter recovery time
- 2) Reduced temperature dependence
- 3) High-speed switching possible

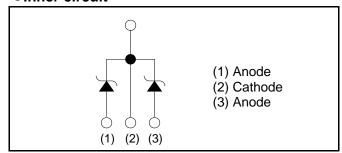
Construction

Silicon carbide epitaxial planer type

Outline



•Inner circuit



Packaging specifications

Туре	Packaging	Tube			
	Reel size (mm)	-			
	Tape width (mm)	-			
	Basic ordering unit (pcs)	30			
	Packing code	С			
	Marking	SCS220AE2			

● Absolute maximum ratings (Ti = 25°C)

Parameter	Symbol	Value	Unit	
Reverse voltage (repetitive peak)	V_{RM}	650	V	
Reverse voltage (DC)	V _R	650	V	
Continuous forward current ^{*7}	I _F	10/20* ¹	А	
		40/80*2	А	
Surge no repetitive forward current*7	I _{FSM}	150/300* ³	А	
		31/63*4	А	
Repetitive peak forward current ^{*7}	I _{FRM}	42/85* ⁵	А	
Total power disspation*7	P _D	83/160* ⁶	W	
Junction temperature	Tj	175	°C	
Range of storage temperature	Tstg	-55 to +175	°C	

^{*1} Tc=137°C/Tc=137°C *2 PW=8.3ms sinusoidal, Tj=25°C *3 PW=10μs square, Tj=25°C

^{*4} PW=8.3ms sinusoidal, Tj=150°C *5 Tc=100°C, Tj=150°C, Duty cycle=10%

^{*6} Tc=25°C *7 Per leg / Both legs

●Electrical characteristics (Tj = 25°C) (Per leg)

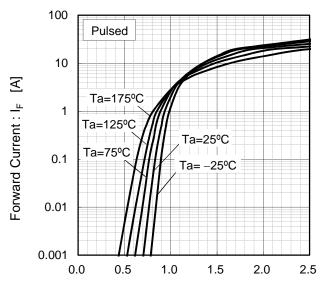
Parameter	Symbol	Conditions	Values			Linit
			Min.	Тур.	Max.	Unit
DC blocking voltage	V_{DC}	I _R =0.2mA	600	-	-	V
Forward voltage	V _F	I _F =10A,Tj=25°C	-	1.35	1.55	V
		I _F =10A,Tj=150°C	-	1.55	-	V
		I _F =10A,Tj=175°C	-	1.63	-	V
Reverse current	I _R	V _R =600V,Tj=25°C	-	2	200	μΑ
		V _R =600V,Tj=150°C	-	30	-	μΑ
		V _R =600V,Tj=175°C	-	70	-	μΑ
Total capacitance	С	V _R =1V,f=1MHz	-	365	-	pF
		V _R =600V,f=1MHz	-	37	-	pF
Total capacitive charge	Qc	V _R =400V,di/dt=350A/μs	-	15	-	nC
Switching time	tc	V _R =400V,di/dt=350A/μs	-	15	-	ns

Thermal characteristics

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Offic
Thermal resistance	$R_{\text{th(j-c)}}$	Per Leg	ı	1.6	1.8	°C/W
		Both Legs	1	0.8	0.9	°C/W

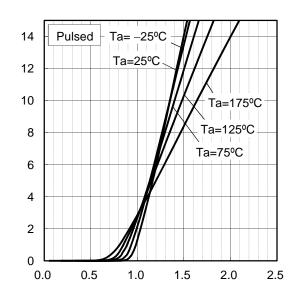
•Electrical characteristic curves

Fig.1 V_F - I_F Characteristics (Per leg)



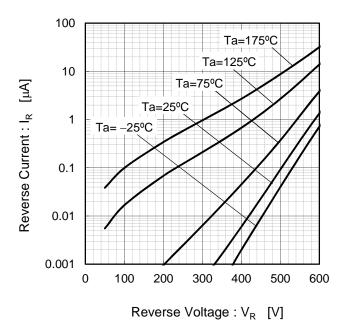
Forward Voltage : V_F [V]

Fig.2 V_F - I_F Characteristics (Per leg)



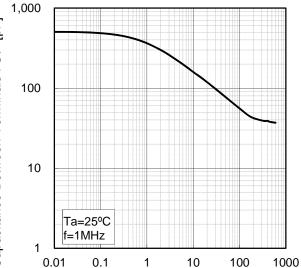
Forward Voltage : V_F [V]

Fig.3 V_R - I_R Characteristics (Per leg)



Capacitance Between Terminals: Ct [pF]

Forward Current : IF [A]



1

0.1

Fig.4 V_R-Ct Characteristics (Per leg)

Reverse Voltage : V_R [V]

100

•Electrical characteristic curves

Fig.5 Thermal Resistance
vs. Pulse Width (Per leg)

10

Ta=25°C
Single Pulse

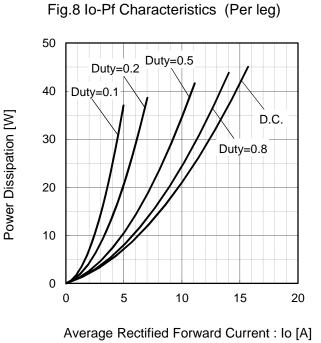
0.01
0.0001 0.001 0.01 0.1 1 10 100 1000

Pulse Width: Pw [s]

Fig.6 Power Dissipation (Per leg)

Case Temperature: Tc [°C]

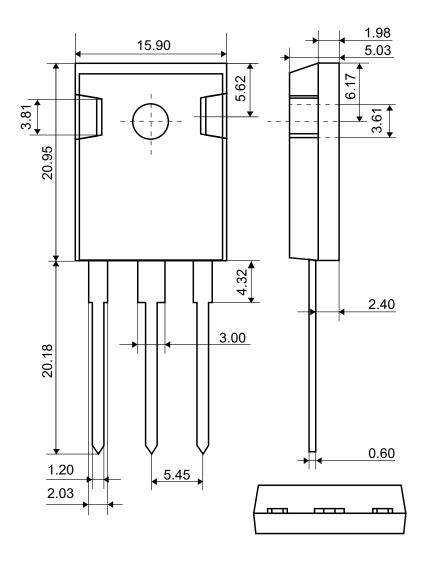
Fig.7 Derating Curve Ip-Tc (Per leg) Duty=0.1 Peak Forward Current : Ip [A] Duty=0.2 Duty=0.5 Duty=0.8 D.C. Case Temperature : Tc [°C]



Power Dissipation [W]

●Dimensions (Unit : mm)

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