

L4941

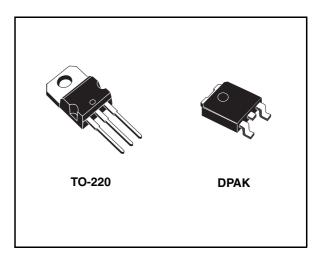
Very low drop 1A regulator

Feature summary

- Low dropout voltage (450mV typ. at 1A)
- Very low quiescent current
- Thermal shutdown
- Short circuit protection
- Reverse polarity protection

Description

The L4941 is a three terminal 5V positive regulators available in TO-220 and DPAK packages, making it useful in a wide range of industrial and consumer applications. Thanks to its very low input/output voltage drop, these devices are particularly suitable for battery powered equipments, reducing consumption and



prolonging battery life. It employs internal current limiting, antisaturation circuit, thermal shut-down and safe area protection.

Order code

Part number	Package
L4941BV	TO-220
L4941BDT-TR	DPAK

September 2006

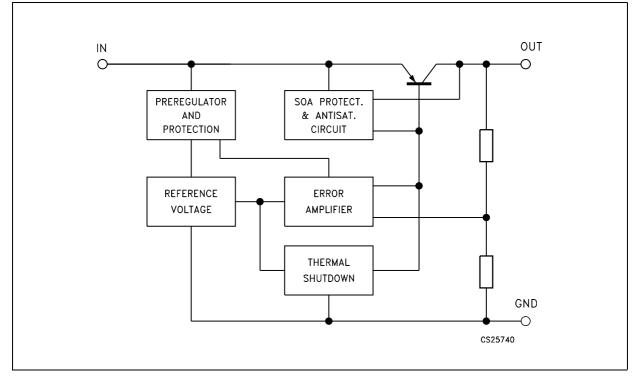
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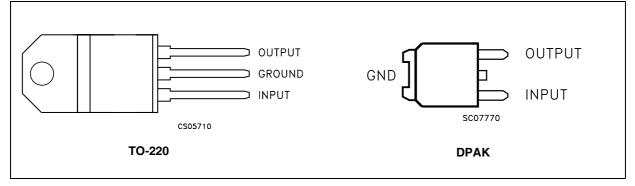
1 Block diagram

Figure 1. Block diagram



2 Pin configuration







3 Maximum ratings

Symbol	Parameter	Value	Unit
VI	Forward input voltage	30	V
V _{IR}	Reverse input voltage (R _O =100Ω)	-15	V
Ι _Ο	Output current	Internally Limited	mA
P _D	Power dissipation	Internally Limited	mW
T _{stg}	Storage temperature range	-40 to +150	°C
T _{op}	Operating junction temperature range	-40 to +150	°C

Table 1. Absolute maximum ratings

Note: Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied

Table 2.	i nermai Data			
Symbol	Parameter	TO-220	DPAK	Unit
R _{thJC}	Thermal resistance junction-case	3	8	°C/W
R _{thJA}	Thermal resistance junction-ambient	50	100	°C/W

Table 2. Thermal Data

4 **Test circuits**



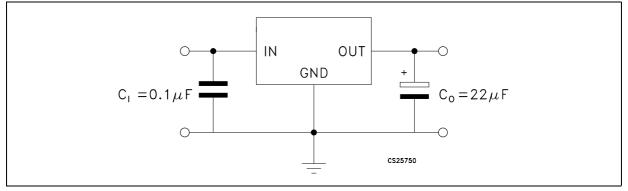


Figure 4. Load rejection

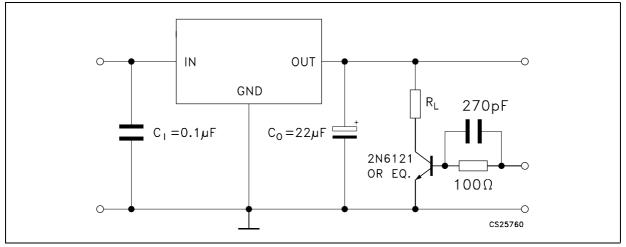
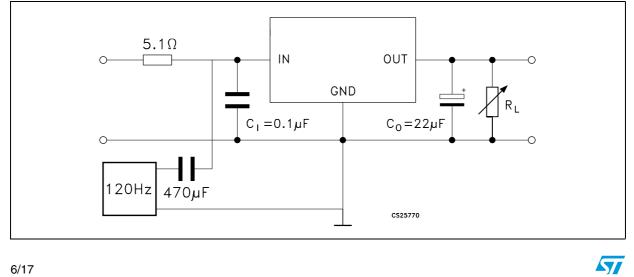


Figure 5. **Ripple rejection**



L4941

5 Electrical characteristics

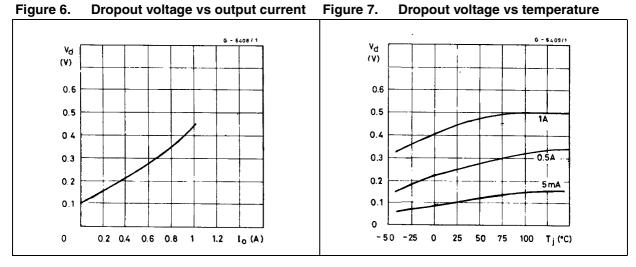
Table 3.Electrical characteristics (refer to test circuit, $V_I=7V$, $C_I = 0.1\mu$ F, $C_O = 22\mu$ F, $T_J = 25^{\circ}$ C,
unless otherwise specified.)

0	Danamatan	,	N.41	T		1114
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	$I_{O} = 5$ mA to 1A, $V_{I} = 6$ to 14V	4.8	5	5.2	V
VI	Input voltage	I _O = 5 mA			16	V
ΔV_O	Line regulation	$V_{I} = 6$ to 16V, $I_{O} = 5$ mA		5	20	mV
A) (Lood regulation	$I_{O} = 5mA$ to 1A		8	20	mV
ΔV_{O}	Load regulation	I _O = 0.5A to 1A		5	15	mV
1	Quipagent ourrent	$I_0 = 5 \text{ mA}, V_1 = 6V$		4	8	mA
Ι _q	Quiescent current	$I_0 = 1A, V_1 = 6V$		20	40	mA
41	Quipagent ourrant change	$I_0 = 5 \text{ mA}, V_1 = 6 \text{ to } 14 \text{V}$			3	mA
ΔI_q	Quiescent current change $I_0 = 1A$	$I_0 = 1A, V_1 = 6 \text{ to } 14V$			-10	mA
V	/ D	I _O = 0.5A		250	450	mV
V _d	Dropout voltage	I _O = 1A		450	700	mV
$\Delta V_O / \Delta T$	Output voltage drift			0.6		mV/°C
SVR	Supply voltage rejection	f = 120Hz, I _O = 1A	58	68		dB
I	Chart size it surrout	V ₁ = 14V		1.6	2.0	^
I _{sc}	Short circuit current	$V_{I} = 6V$		1.8	2.2	A
Z _O	Output impedance	f = 1KHz, I _O = 0.5A		30		mΩ
e _N	Output noise voltage	B = 100Hz to 100KHz		30		μV/V _O



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6 Typical application





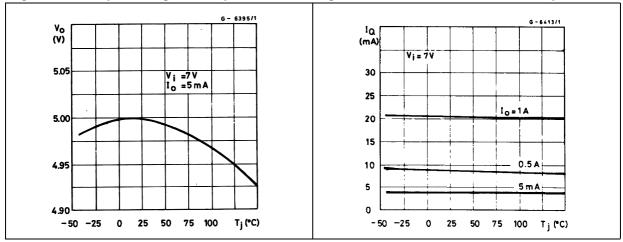
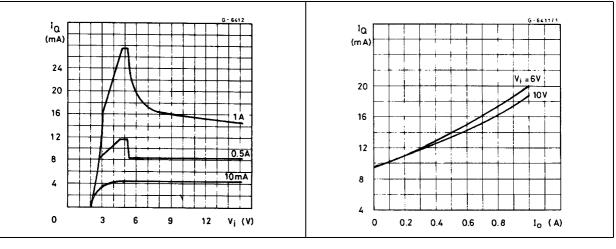


Figure 9.

Quiescent current vs temperature





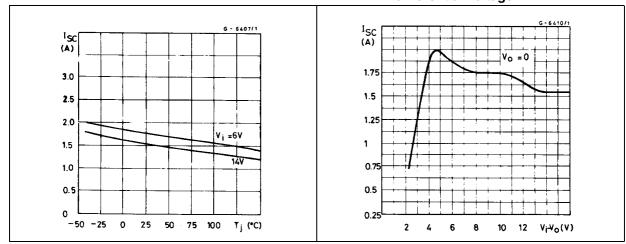


Figure 12. Short circuit current vs temperature Figure 13. Peak output current vs input/output differential voltage



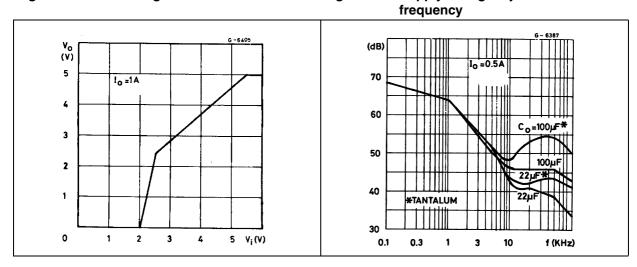


Figure 15.

Supply voltage rejection vs

Figure 16. Supply voltage rejection vs output Figure 17. Load dump characteristics current

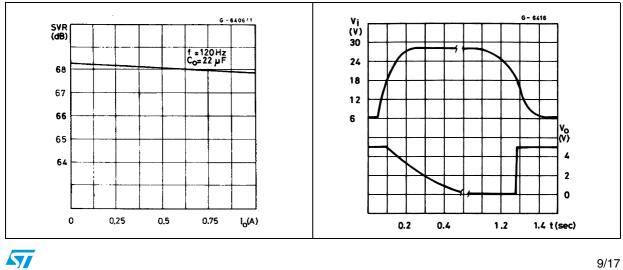


Figure 18. Line transient response

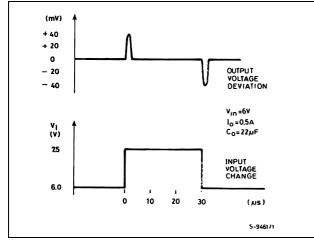


Figure 20. Load transient response

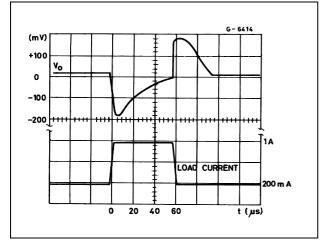
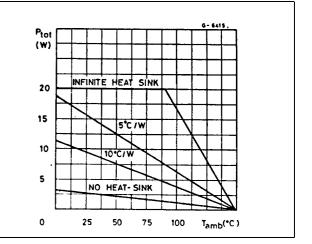


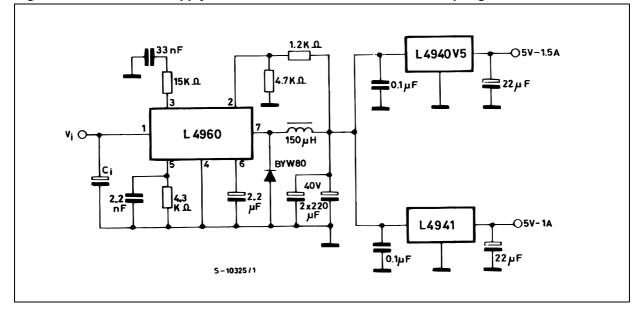
Figure 19. Total power dissipation





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7 Schematic application



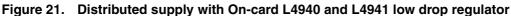
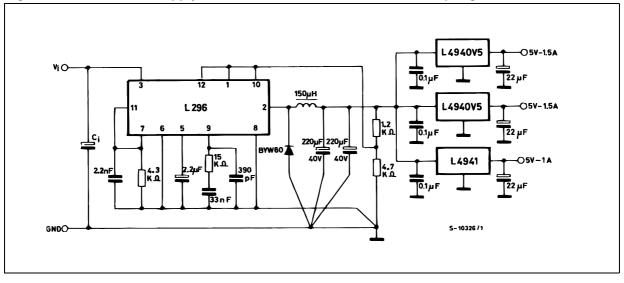


Figure 22. Distributed Supply with On-card L4940 and L4941 low drop regulator



ADVANTAGES OF THESE APPLICATION ARE:

On card regulation with short-circuit and thermal protection on each output.

Vary high total system efficiency due to the switching preregulation and very low-drop postregulation.

8 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a Lead-free second level interconnect. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.



MAX. 0.181 0.051 0.107

0.027 0.034 0.067 0.067 0.203 0.106

0.409

0.551

0.116

0.620

0.260

0.154

0.151

	TO-220 MECHANICAL DATA						
DIM		mm.			inch		
DIM.	MIN.	ТҮР	MAX.	MIN.	TYP.	Τ	
А	4.40		4.60	0.173			
С	1.23		1.32	0.048		T	
D	2.40		2.72	0.094		T	
D1		1.27			0.050	T	
Е	0.49		0.70	0.019			
F	0.61		0.88	0.024		1	
F1	1.14		1.70	0.044		1	
F2	1.14		1.70	0.044		T	
G	4.95		5.15	0.194		1	
						_	

16.4

2.7

10.40

14.0

2.95

15.75

6.6

3.93

3.85

0.094

0.393

0.511

0.104

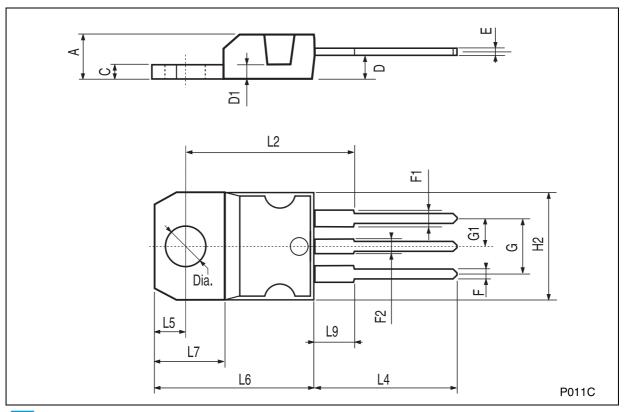
0.600

0.244

0.137

0.147

0.645



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G1

H2

L2

L4

L5

L6

L7

L9

DIA.

2.4

10.0

13.0

2.65

15.25

6.2

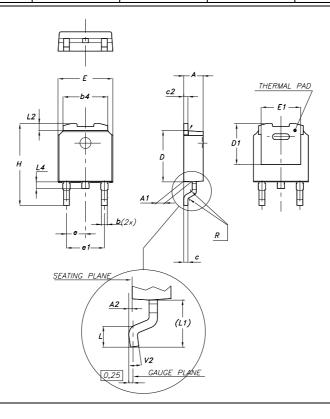
3.5

3.75

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DPAK MECHANICAL DATA

DIM		mm.				
DIM.	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A2	0.03		0.23	0.001		0.009
В	0.64		0.9	0.025		0.035
b4	5.2		5.4	0.204		0.212
С	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
D1		5.1			0.200	
E	6.4		6.6	0.252		0.260
E1		4.7			0.185	
е		2.28			0.090	
e1	4.4		4.6	0.173		0.181
Н	9.35		10.1	0.368		0.397
L	1			0.039		
(L1)		2.8			0.110	
L2		0.8			0.031	
L4	0.6		1	0.023		0.039
R		0.2			0.008	
V2	0°		8°	0°		8°

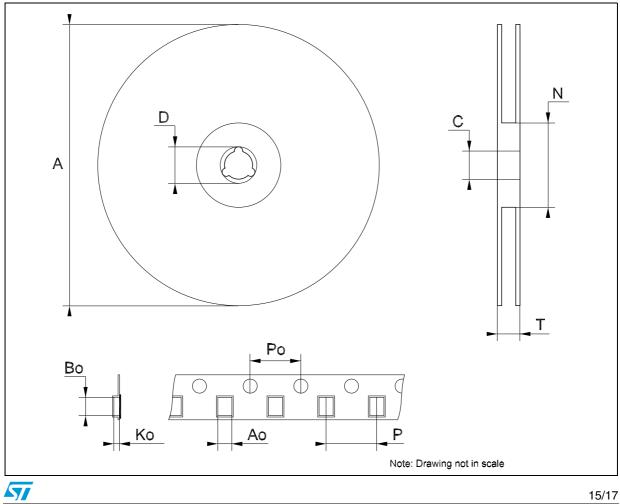


0068772-F



		mm.			inch	
DIM.	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
А			330			12.992
С	12.8	13.0	13.2	0.504	0.512	0.519
D	20.2			0.795		
Ν	60			2.362		
Т			22.4			0.882
Ao	6.80	6.90	7.00	0.268	0.272	0.2.76
Во	10.40	10.50	10.60	0.409	0.413	0.417
Ko	2.55	2.65	2.75	0.100	0.104	0.105
Po	3.9	4.0	4.1	0.153	0.157	0.161
Р	7.9	8.0	8.1	0.311	0.315	0.319

Tape & Reel DPAK-PPAK MECHANICAL DATA



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9 Revision history

Date	Revision	Changes	
21-Jun-2004	4	Document updating.	
15-Sep-2006	5	Order Codes has been updated and new template.	



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