

MD2310FX

High voltage NPN power transistor for standard definition CRT display

Features

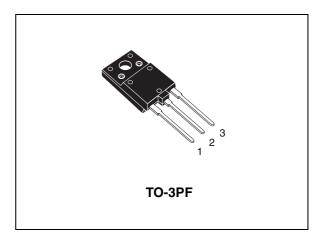
- State-of-the-art technology:
 - diffused collector "enhanced generation"
- Stable performance versus operating temperature variation
- Low base drive requirement
- Tight h_{FE} range at operating collector current
- Fully insulated power package U.L. compliant

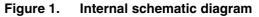
Application

 Horizontal deflection output for monitor and real flat TV

Description

The MD2310FX is manufactured using planar technology with diffused collector adopting new and enhanced high voltage structure. The MD product series show improved silicon efficiency bringing updated performance to the horizontal deflection stage.





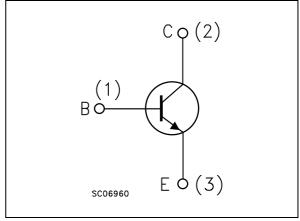


Table 1.Device summary

Order code	Marking	Package	Packing
MD2310FX	MD2310FX	TO-3PF	Tube

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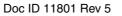
1 Electrical ratings

	Absolute maximum rutings					
Symbol	Parameter	Value	Unit			
V _{CES}	Collector-emitter voltage ($V_{BE} = 0$)	1500	V			
V_{CEO}	Collector-emitter voltage ($I_B = 0$)	700	V			
V_{EBO}	Emitter-base voltage ($I_C = 0$)	9	V			
Ι _C	Collector current	14	Α			
I _{CM}	Collector peak current (t _P < 5 ms)	21	Α			
Ι _Β	Base current	7	Α			
P _{TOT}	Total dissipation at $T_c = 25 \text{ °C}$	62	W			
V _{INS} Insulation withstand voltage (RMS) from all three leads to external heatsink		2500	v			
T _{STG}	Storage temperature	-65 to 150	°C			
TJ	Max. operating junction temperature 150					

Table 2. Absolute maximum ratings

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance junction-case Max	2	°C/W



2 Electrical characteristics

 T_{CASE} = 25 °C; unless otherwise specified.

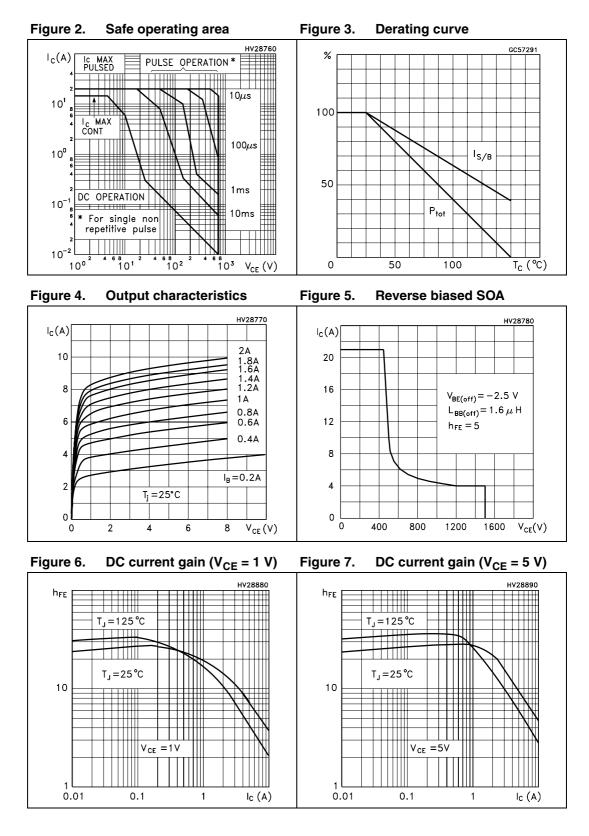
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current	V _{CE} = 1500 V			0.2	mA
020	(V _{BE} = 0)	$V_{CE} = 1500 V$ $T_{c} = 125 °C$			2	mA
	Emitter cut-off current	V _{FB} = 9 V			1	mA
I _{EBO}	$(I_{\rm C} = 0)$	VEB - 5 V				IIIA
V _{CEO(sus)}	Collector-emitter sustaining voltage $(I_B = 0)$	I _C = 100 mA	700			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 7 A I _B = 1.75 A			2.5	V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = 7 A I _B = 1.75 A			1.1	V
		$I_{C} = 1 A$ $V_{CE} = 5 V$		28		
h _{FE} ⁽¹⁾	DC current gain	$I_{C} = 7 A$ $V_{CE} = 1 V$		5.5		
		$I_{\rm C} = 7 {\rm A}$ $V_{\rm CE} = 5 {\rm V}$	6		8.5	
	INDUCTIVE LOAD	$I_{\rm C} = 6 \text{ A}$ $f_{\rm h} = 64 \text{ kHz}$				
t _s	Storage time	$I_{B(on)} = 0.9 \text{ A}$ $V_{BE(off)} = -2.7 \text{ V}$		2.3	2.8	μs
t _f	Fall time	$L_{BB(off)} = 1.6 \ \mu H$		0.12	0.25	μs

 Table 4.
 Electrical characteristics

1. Pulse test: pulse duration \leq 300 µs, duty cycle \leq 2 %.

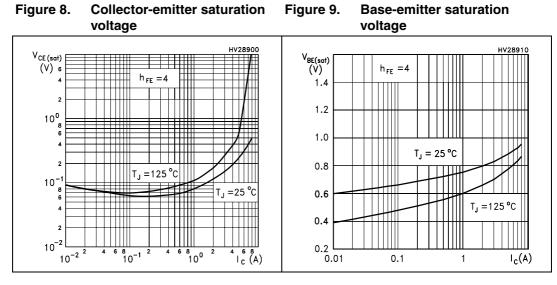


2.1 Typical characteristics



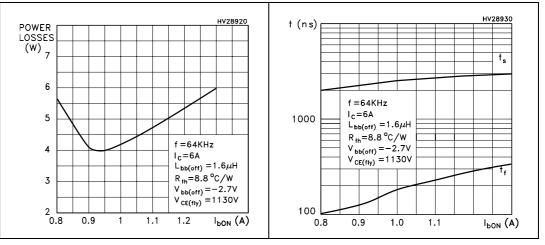
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3 Test circuits

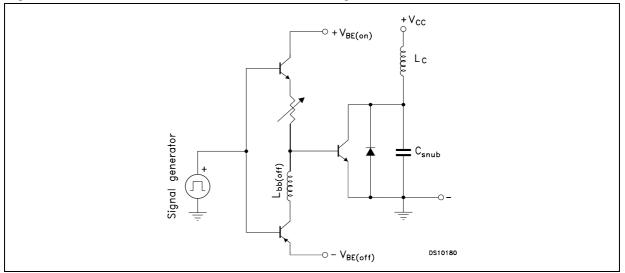
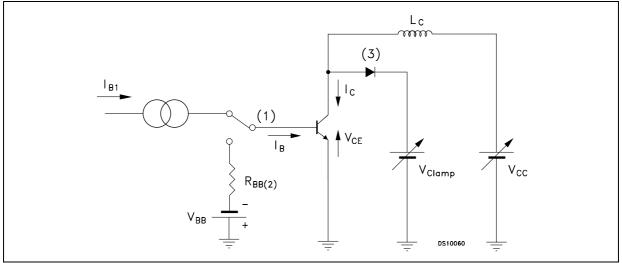


Figure 12. Power losses and inductive load switching test circuit





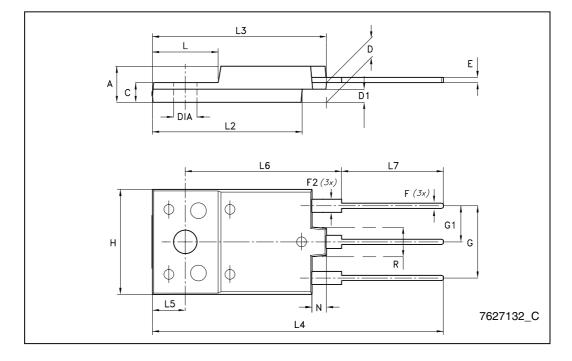


4 Package mechanical data

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	TO-3PF mechanical data				
DIM.	mm.				
Dim.	min.	typ	max.		
A	5.30		5.70		
С	2.80		3.20		
D	3.10		3.50		
D1	1.80		2.20		
E	0.80		1.10		
F	0.65		0.95		
F2	1.80		2.20		
G	10.30		11.50		
G1		5.45			
Н	15.30		15.70		
L	9.80	10	10.20		
L2	22.80		23.20		
L3	26.30		26.70		
L4	43.20		44.40		
L5	4.30		4.70		
L6	24.30		24.70		
L7	14.60		15		
N	1.80		2.20		
R	3.80		4.20		
Dia	3.40		3.80		





5 Revision history

Table 5.Document revision history

Date	Revision	Changes
18-Oct-2005	1	First release
25-Nov-2005	2	Complete datasheet
15-Dec-2005	3	Legal page inserted
29-Sep-2006	4	New h _{FE} limit
27-Oct-2009	5	Updated TO-3PF package mechanical data



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