

STGP10M65DF2

Trench gate field-stop IGBT, M series 650 V, 10 A low loss

Datasheet - production data

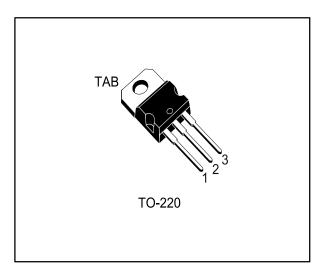
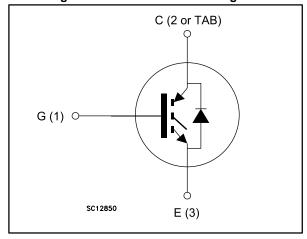


Figure 1: Internal schematic diagram



Features

- 6 µs of short-circuit withstand time
- V_{CE(sat)} = 1.55 V (typ.) @ I_C = 10 A
- Tight parameter distribution
- Safer paralleling
- Low thermal resistance
- Soft and very fast recovery antiparallel diode

Applications

- Motor control
- UPS
- PFC

Description

This device is an IGBT developed using an advanced proprietary trench gate field-stop structure. The device is part of the M series of IGBTs, which represents an optimum compromise in performance to maximize the efficiency of inverter systems where low loss and short-circuit capability are essential. Furthermore, a positive $V_{\text{CE(sat)}}$ temperature coefficient and tight parameter distribution result in safer paralleling operation.

Table 1: Device summary

Order code	Marking	Package	Packing
STGP10M65DF2	G10M65DF2	TO-220	Tube

Contents STGP10M65DF2

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STGP10M65DF2 Electrical ratings

1 Electrical ratings

Table 2: Absolute maximum ratings

Symbol	Parameter Value		Unit
Vces	Collector-emitter voltage (V _{GE} = 0)	650	V
1.	Continuous collector current at T _C = 25 °C	20	۸
lc	Continuous collector current at T _C = 100 °C	10	Α
ICP ⁽¹⁾	Pulsed collector current	40	Α
V_{GE}	Gate-emitter voltage	± 20	V
I_	Continuous forward current at T _C = 25 °C	20	А
IF	Continuous forward current at T _C = 100 °C	10	A
I _{FP} ⁽¹⁾	Pulsed forward current	40	Α
Ртот	Total dissipation at T _C = 25 °C	115	W
T _{STG}	Storage temperature range - 55 to 150		°C
TJ	Operating junction temperature	- 55 to 175	J

Notes:

Table 3: Thermal data

Symbo	Parameter	Value	Unit
RthJC	Thermal resistance junction-case IGBT	1.3	
RthJC	R _{thJC} Thermal resistance junction-case diode 2		°C/W
RthJA	Thermal resistance junction-ambient	62.5	

 $^{^{(1)}}$ Pulse width limited by maximum junction temperature.

Electrical characteristics STGP10M65DF2

2 Electrical characteristics

T_C = 25 °C unless otherwise specified

Table 4: Static characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)CES}	Collector-emitter breakdown voltage	V _{GE} = 0 V, I _C = 2 mA	650			V
		V _{GE} = 15 V, I _C = 10 A		1.55	2.0	
V _{CE(sat)}	V _{CE(sat)} Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 10 A, T _J = 125 °C		1.9		V
		V _{GE} = 15 V, I _C = 10 A, T _J = 175 °C		2.1		
		I _F = 10 A		1.5		
VF	Forward on-voltage	I _F = 10 A, T _J = 125 °C		1.3		V
		I _F = 10 A, T _J = 175 °C		1.2		
V _{GE(th)}	Gate threshold voltage	V _{CE} = V _{GE} , I _C = 250 μA	5	6	7	V
Ices	Collector cut-off current	V _{GE} = 0 V, V _{CE} = 650 V			25	μΑ
I _{GES}	Gate-emitter leakage current	V _{CE} = 0 V, V _{GE} = ± 20 V			250	μΑ

Table 5: Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Cies	Input capacitance		-	840	-	
Coes	Output capacitance	V _{CE} = 25 V, f = 1 MHz, V _{GE} = 0 V	-	63	-	рF
Cres	Reverse transfer capacitance		-	16	-	
Qg	Total gate charge		-	28	ı	
Q _{ge}	Gate-emitter charge	V_{CC} = 520 V, I_{C} = 10 A, V_{GE} = 15 V (see <i>Figure 30: " Gate charge test</i>	-	6	-	nC
Qgc	Gate-collector charge	circuit")	-	12	-	

Table 6: IGBT switching characteristics (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time		-	19	ı	ns
tr	Current rise time	$V_{CE} = 400 \text{ V}, I_{C} = 10 \text{ A}, V_{GE} = 15 \text{ V},$	-	7.4	-	ns
(di/dt) _{on}	Turn-on current slope	$R_G = 22 \Omega$ (see Figure 29: "Test circuit for inductive load switching")	-	1086	-	A/µs
t _{d(off)}	Turn-off-delay time		-	91	-	ns

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _f	Current fall time		-	92	-	ns
E _{on} ⁽¹⁾	Turn-on switching losses		-	0.12	-	mJ
E _{off} ⁽²⁾	Turn-off switching losses		-	0.27	-	mJ
Ets	Total switching losses		-	0.39	-	mJ
t _{d(on)}	Turn-on delay time		-	18	-	ns
t _r	Current rise time		-	9	-	ns
(di/dt) _{on}	Turn-on current slope		-	890	-	A/µs
t _{d(off)}	Turn-off-delay time	Vce = 400 V, Ic = 10 A, Vge = 15 V,	-	90	-	ns
t _f	Current fall time	$R_G = 22 \Omega T_J = 175 ^{\circ} C$ (see Figure 29: " Test circuit for inductive load switching")	-	170	-	ns
Eon	Turn-on switching losses		-	0.26	-	mJ
Eoff	Turn-off switching losses		-	0.4	-	mJ
E _{ts}	Total switching losses		-	0.66	-	mJ
t _{sc}	Short-circuit withstand time	V _{CC} ≤ 400 V, V _{GE} = 15 V, T _{Jstart} = 150 °C	6		-	μs

Notes:

Table 7: Diode switching characteristics (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{rr}	Reverse recovery time		-	96		ns
Qrr	Reverse recovery charge	I _F = 10 A, V _R = 400 V, V _{GE} = 15 V	-	373		nC
I _{rrm}	Reverse recovery current	(see Figure 29: " Test circuit for inductive load switching") di/dt = 1000 A/µs	-	13		Α
dl _{rr} /dt	Peak rate of fall of reverse recovery current during t _b	·	-	661		A/µs

⁽¹⁾Energy losses include reverse recovery of the diode.

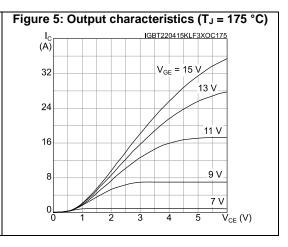
 $[\]ensuremath{^{(2)}}\mbox{Turn-off losses}$ also include the tail of the collector current.

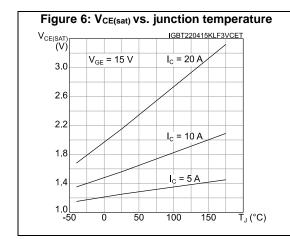
Electrical characteristics

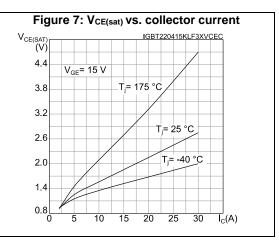
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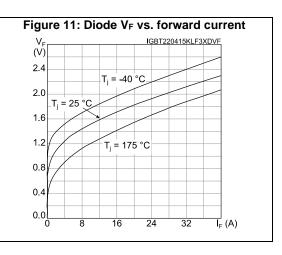
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Err	Reverse recovery energy		-	52		μJ
t _{rr}	Reverse recovery time		-	201		ns
Qrr	Reverse recovery charge		-	1352		nC
I _{rrm}	Reverse recovery current	IF = 10 A, V_R = 400 V, V_{GE} = 15 V T_J = 175 °C (see Figure 29: " Test circuit for inductive load switching") di/dt = 1000 A/ μ s	-	19		Α
dl _{rr} /dt	Peak rate of fall of reverse recovery current during t _b		-	405		A/µs
Err	Reverse recovery energy			150		μJ

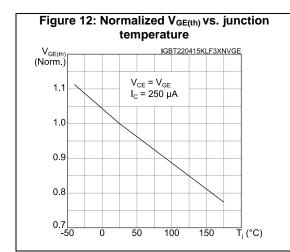
2.1 Electrical characteristics (curves)

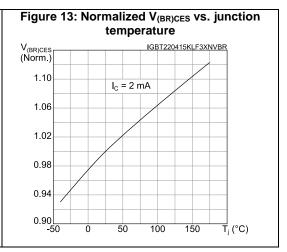












STGP10M65DF2 Electrical characteristics

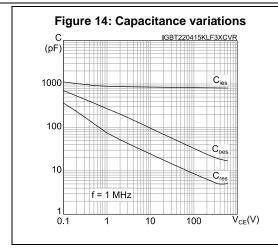
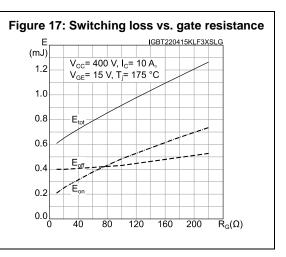
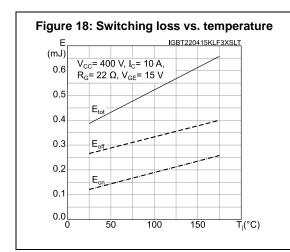
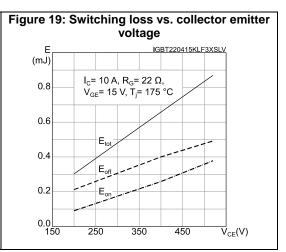


Figure 16: Switching loss vs. collector current E (mJ) IGBT220415KLF3XSLC V_{CC} = 400 V, R_G = 22 Ω 0.8 V_{GE}= 15 V, T_j= 175 °C 0.6 Eoff 0.4 Eon 0.2 0.0 12 16 $\overline{\mathsf{I}}_{\mathsf{C}}(\mathsf{A})$







5

Figure 20: Short-circuit time and current vs. V_{GE} t_{SC} (μs) 20 t_{SC} $V_{CC} \le 400 \text{ V}$ $T_{j} \le 150 \text{ °C}$ 15 10 35

12

13 14 15

20

 $\overrightarrow{V}_{GE}(V)$

Figure 22: Switching times vs. gate resistance

(ns)

t_{d(on)}

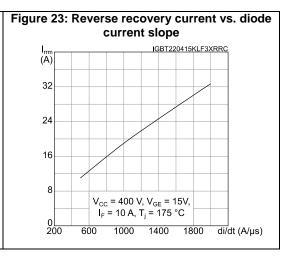
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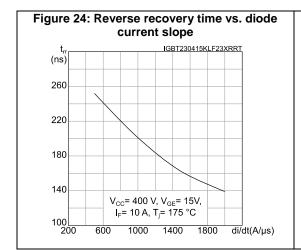
t_{d(on)}

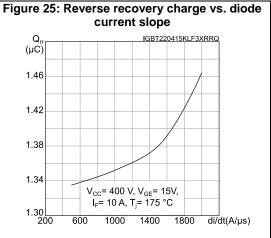
10

V_{CC}= 400 V, V_{GE}= 15V, I_C= 10 A, T_j= 175 °C

0 40 80 120 160 200 R_G(Ω)







STGP10M65DF2 Electrical characteristics

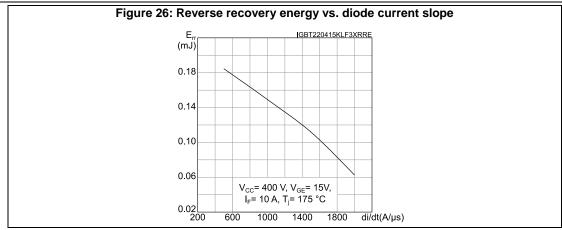


Figure 27: Thermal impedance for IGBT

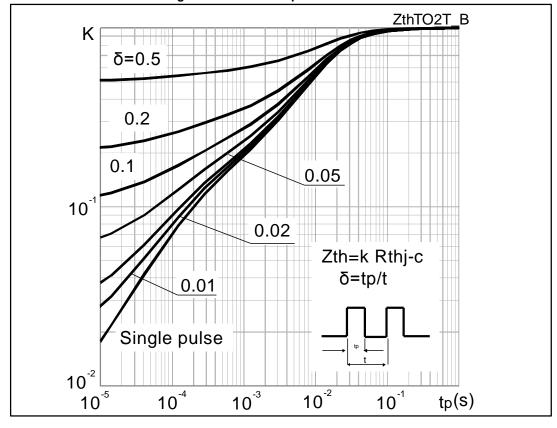
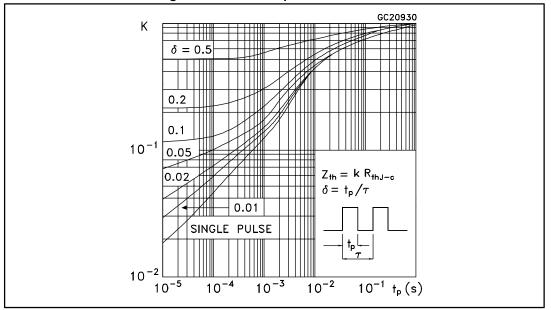
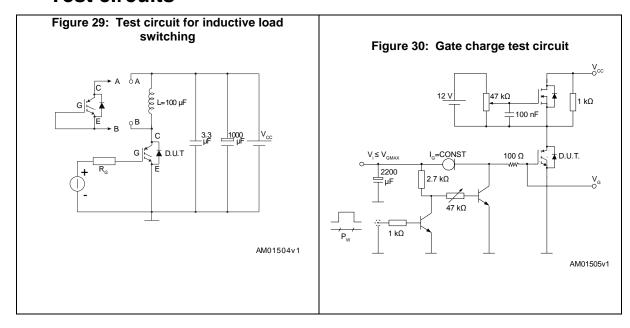


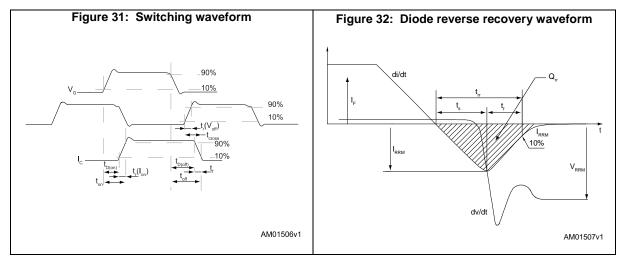
Figure 28: Thermal impedance for diode



STGP10M65DF2 Test circuits

3 Test circuits





4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

STGP10M65DF2 Package information

4.1 TO-220 type A package information

Figure 33: TO-220 type A package outline

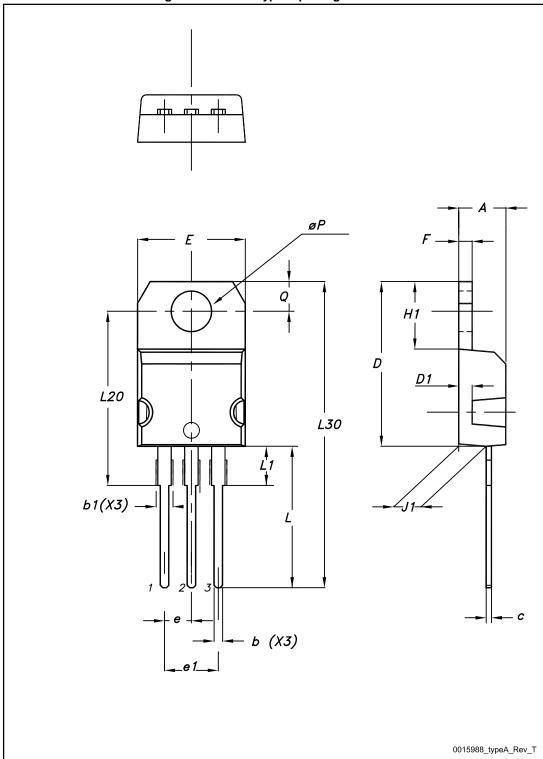


Table 8: TO-220 type A mechanical data

Dim.		mm	
Dilli.	Min.	Тур.	Max.
А	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
С	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
øΡ	3.75		3.85
Q	2.65		2.95

STGP10M65DF2 Revision history

5 Revision history

Table 9: Document revision history

Date	Revision	Changes
10-Feb-2015	1	First release.
23-Apr-2015	2	Minor text edits throughout document Document status promoted to 'Production data' In Section 2 Electrical characteristics: - updated Table 4: Static characteristics - updated Table 5: Dynamic characteristics - updated Table 6: IGBT switching characteristics (inductive load) - updated Table 7: Diode switching characteristics (inductive load) Added Section 2.1 Electrical characteristics (curves)
31-Jul-2015	3	Updated Table 7: Diode switching characteristics (inductive load)
19-Oct-2015	4	Updated Table 5: "Dynamic characteristics" and Table 6: "IGBT switching characteristics (inductive load)". Updated Figure 8: "Collector current vs. switching frequency".

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