

# STL73

## MEDIUM VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- n MEDIUM VOLTAGE CAPABILITY
- n LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- n VERY HIGH SWITCHING SPEED

#### APPLICATIONS

n COMPACT FLUORESCENT LAMPS (CFLS)

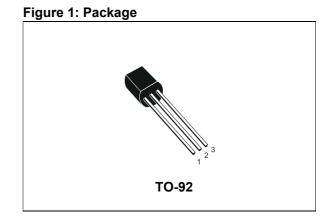
#### DESCRIPTION

The device is manufactured using high voltage Multi-Epitaxial Planar technology for high switching speeds and medium voltage capability.

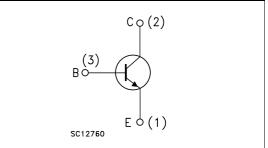
It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The STL series is designed for use in Compact Fluorescent Lamps.

#### Table 1: Order Codes



#### Figure 2: Internal Schematic Diagram



Part Number	Marking	Package	Packaging
STL73	L73 L or (#) L73 H	TO-92	Bulk

# See:note on page 2

### STL73

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-Emitter Voltage (V <sub>BE</sub> = 0)	700	V
V <sub>CEO</sub>	Collector-Emitter Voltage (I <sub>B</sub> = 0)	400	V
$V_{\text{EBO}}$	Emitter-Base Voltage (I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	V
Ι <sub>C</sub>	Collector Current	1.5	Α
I <sub>CM</sub>	Collector Peak Current (t <sub>p</sub> < 5ms)	3	Α
I <sub>B</sub>	Base Current	0.5	Α
I <sub>BM</sub>	Base Peak Current (t <sub>p</sub> < 5ms)	1.5	Α
P <sub>tot</sub>	Total Dissipation at T <sub>C</sub> = 25 °C	1.1	W
T <sub>stg</sub>	Storage Temperature	-65 to 150	°C
ТJ	Max. Operating Junction Temperature	150	°C

#### Table 2: Absolute Maximum Ratings

#### **Table 3: Thermal Data**

R <sub>thj-amb</sub>	Thermal Resistance Junction-Ambient	Max	112	°C/W

## Table 4: Electrical Characteristics ( $T_{case}$ = 25 °C unless otherwise specified)

Symbol	Parameter	Test Co	onditions	Min.	Тур.	Max.	Unit
I <sub>CEV</sub>	Collector Cut-off Current	V <sub>CE</sub> = 700 V				1	mA
	(V <sub>BE</sub> = -1.5 V)	V <sub>CE</sub> = 700 V	T <sub>j</sub> = 125 <sup>o</sup> C			5	mA
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	i <sub>E</sub> = 10 mA		9		18	V
	$(I_{\rm C} = 0)$						
V <sub>CEO(sus)</sub> *	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = 10 mA		400			V
	$(I_{B} = 0)$						
V <sub>CE(sat)</sub> *	Collector-Emitter	I <sub>C</sub> = 0.3 A	I <sub>B</sub> = 60 mA		0.15	0.4	V
	Saturation Voltage	I <sub>C</sub> = 0.6 A	I <sub>B</sub> = 120 mA		0.25	0.6	V
		I <sub>C</sub> = 1 A	I <sub>B</sub> = 250 mA		0.4	1	V
V <sub>BE(sat)</sub> *	Base-Emitter Saturation Voltage	I <sub>C</sub> = 0.6 A	I <sub>B</sub> = 120 mA		0.95	1.1	V
h <sub>FE</sub>	DC Current Gain #	I <sub>C</sub> = 0.6 A	V <sub>CE</sub> = 3 V				
		Group L		10		16	
		Group H		15		21	
		I <sub>C</sub> = 1.5 A	V <sub>CE</sub> = 5 V	4		10	
	RESISTIVE LOAD	I <sub>C</sub> = 1	V <sub>CC</sub> = 125 V				
t <sub>f</sub>	Rise Time	I <sub>B1</sub> = -I <sub>B2</sub> = 200 mA	t <sub>p</sub> = 25 μs			1	μs
	Storage Time	(see figure 4)				4	μs
	Fall Time					0.7	μs
	INDUCTIVE LOAD	I <sub>C</sub> = 0.3	V <sub>Clamp</sub> = 300 V				
t <sub>f</sub>	Fall Time	I <sub>B1</sub> = -I <sub>B2</sub> = 60 mA	L = 3 mH		0.3		μs
		(see figure 3)					

\* Pulsed: Pulsed duration = 300  $\mu s,$  duty cycle  $\leq 1.5$  %.

# The product is pre-selected in DC current gain (Group L and Group H). STMicroelectronics reserves the right to ship either groups according to production availability. Please contact your nearest STMicroelectronics sales office for delivery datails.

#### Figure 3: Inductive Load Switching Test Circuit

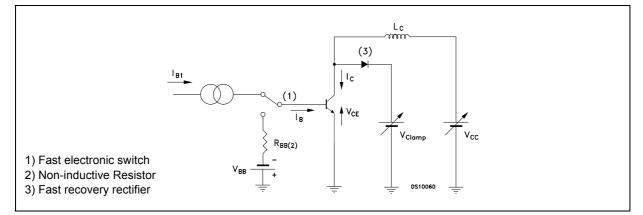
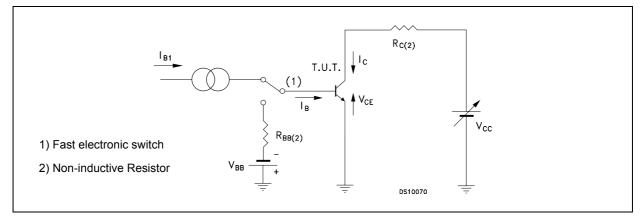
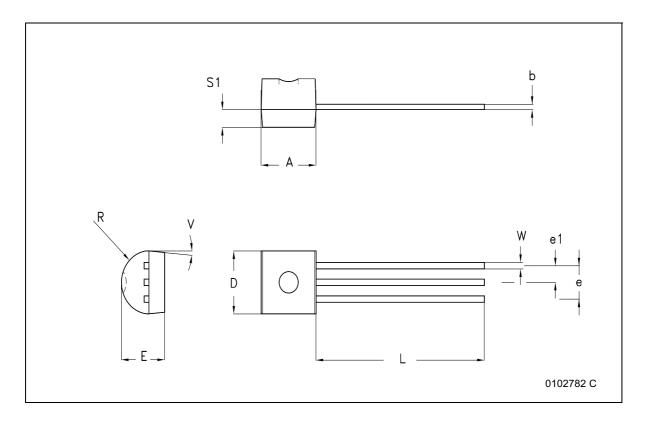


Figure 4: Restistive Load Switching Test Circuit



## **TO-92 BULK SHIPMENT MECHANICAL DATA**

DIM.	mm.					
DIWI.	MIN.	ТҮР	MAX.			
А	4.32		4.95			
b	0.36		0.51			
D	4.45		4.95			
E	3.30		3.94			
e	2.41		2.67			
e1	1.14		1.40			
L	12.70		15.49			
R	2.16		2.41			
S1	0.92		1.52			
w	0.41		0.56			
V		5 <sup>0</sup>				



#### Figure 5: Revision History

Release Date	Version	Change Designator
11-Jul-2005	1	First Release.



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