

# STN851-A

## Low voltage fast-switching NPN power transistor

### Features

- AEC Q101 compliant
- Very low collector to emitter saturation voltage
- High current gain characteristic
- Fast-switching speed
- Surface-mounting SOT-223 power package in tape and reel

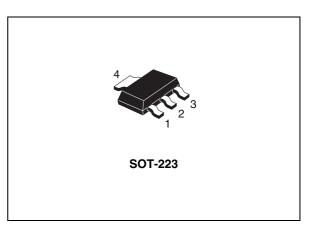
## **Applications**

 High efficiency low voltage switching applications

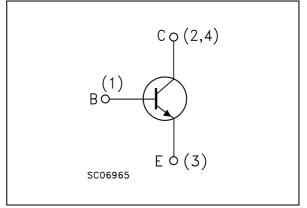
## Description

The device is manufactured in planar technology with "Base Island" layout.

The resulting transistor shows exceptional high gain performance coupled with very low saturation voltage.



### Figure 1. Internal schematic diagram



#### Table 1. Device summary

Order code	Marking	Package	Packaging
STN851-A	N851	SOT-223	Tape and reel

# 1 Electrical ratings

Table 2.	Absolute maximum ratings

Symbol	Parameter	Value	Unit	
V <sub>CBO</sub>	Collector-base voltage (I <sub>E</sub> = 0)	150	V	
V <sub>CEO</sub>	Collector-emitter voltage $(I_B = 0)$	60	V	
V <sub>EBO</sub>	Emitter-base voltage (I <sub>C</sub> = 0)	7	V	
۱ <sub>C</sub>	Collector current	5	Α	
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	10	Α	
Ι <sub>Β</sub>	Base current	1	Α	
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5 ms)	2	Α	
P <sub>tot</sub>	Total dissipation at T <sub>amb</sub> = 25 °C	1.6	W	
T <sub>stg</sub>	Storage temperature	-65 to 150	°C	
Τ <sub>J</sub>	Max. operating junction temperature	150	°C	

### Table 3. Thermal data

Symbol	Parameter	Value	Unit
R <sub>thj-amb</sub>	Thermal resistance junction-ambient <sup>(1)</sup>	78	°C/W

1. Device mounted on a p.c.b. area of 1  $\mbox{cm}^2$ 



## 2 Electrical characteristics

(T<sub>case</sub> = 25 °C unless otherwise specified)

Table 4.	Electrical characteristics						
Symbol	Parameter	Test co	nditions	Min.	Тур.	Max.	Unit
I <sub>CBO</sub>	Collector cut-off current $(I_E = 0)$	V <sub>CB</sub> = 120 V V <sub>CB</sub> = 120 V	T <sub>c</sub> = 100 °C			50 1	nA μA
I <sub>EBO</sub>	Emitter cut-off current $(I_{C} = 0)$	V <sub>EB</sub> = 7 V				10	nA
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage (I <sub>E</sub> = 0)	Ι <sub>C</sub> = 100 μΑ		150			v
V <sub>(BR)CEO</sub> <sup>(1)</sup>	Collector-emitter breakdown voltage $(I_B = 0)$	I <sub>C</sub> = 10 mA		60			v
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage ( $I_{\rm C} = 0$ )	I <sub>E</sub> = 100 μA		7			v
V <sub>CE(sat)</sub> <sup>(1)</sup>	Collector-emitter saturation voltage	$I_C = 100 \text{ mA}$ $I_C = 1 \text{ A}$ $I_C = 2 \text{ A}$ $I_C = 5 \text{ A}$	$I_B = 5 mA$ $I_B = 50 mA$ $I_B = 50 mA$ $I_B = 200 mA$		10 70 140 320	50 120 250 500	mV mV mV mV
V <sub>BE(sat)</sub> <sup>(1)</sup>	Base-emitter saturation voltage	I <sub>C</sub> = 4 A	I <sub>B</sub> = 200 mA		1	1.15	V
V <sub>BE(on)</sub> <sup>(1)</sup>	Base-emitter on voltage	$I_{\rm C} = 4$ A	$V_{CE} = 1 V$		0.89	1	V
$h_{FE}^{(1)}$	DC current gain	$I_{C} = 10 \text{ mA}$ $I_{C} = 2 \text{ A}$ $I_{C} = 5 \text{ A}$ $I_{C} = 10 \text{ A}$	V <sub>CE</sub> = 1 V	150 150 90 30	300 270 140 50	350	
f <sub>T</sub>	Transition frequency	V <sub>CE</sub> = 10 V	I <sub>C</sub> = 100 mA		130		MHz
C <sub>CBO</sub>	Collector-base capacitance (I <sub>E</sub> = 0)	V <sub>CB</sub> = 10 V	f = 1 MHz		50		pF
t <sub>on</sub> t <sub>s</sub> t <sub>f</sub>	Resistive load Turn-on time Storage time Fall time	$I_{\rm C} = 1 \text{ A}$ $I_{\rm B1} = -I_{\rm B2} = 0.7$			50 1.35 120		ns µs ns

Table 4.	Electrical	characteristics
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1. Pulse duration = 300  $\mu s,$  duty cycle  $\leq 1.5\%$ 



Figure 2.

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## 2.1 Electrical characteristics (curves)

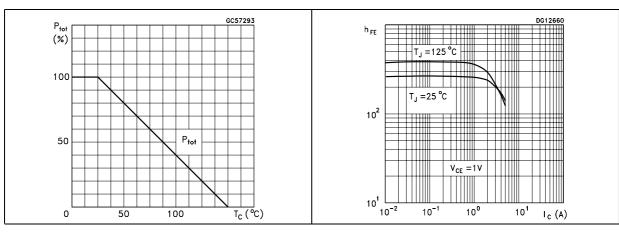


Figure 3.

DC current gain

**Collector-emitter saturation** 

# Figure 4. Collector-emitter saturation voltage

**Derating curve** 

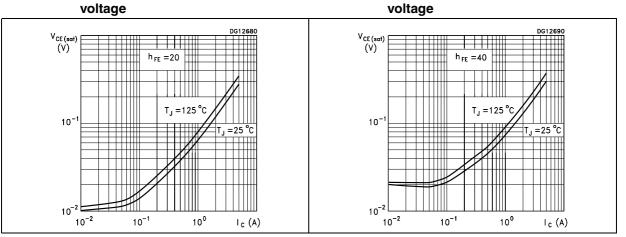
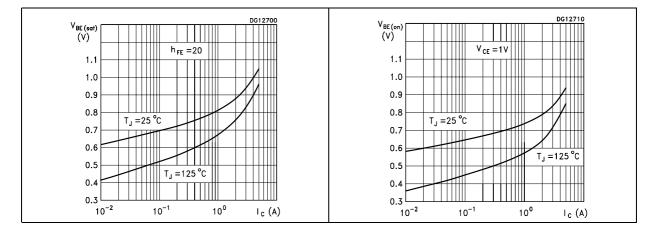


Figure 5.

Figure 6. Base-emitter saturation voltage

Figure 7. Base-emitter on voltage



### Figure 8. Resistive load switching time

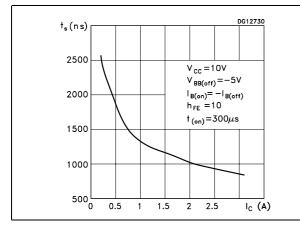


Figure 10. Resistive load switching time

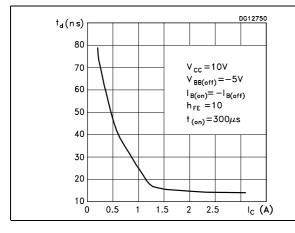
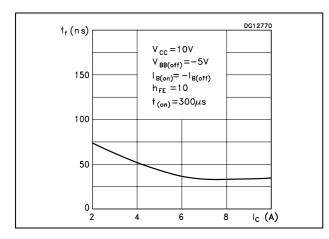


Figure 12. Inductive load switching time



### Figure 9. Resistive load switching time

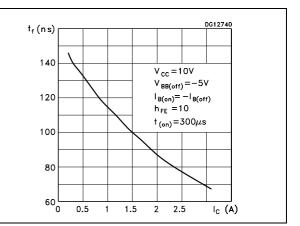
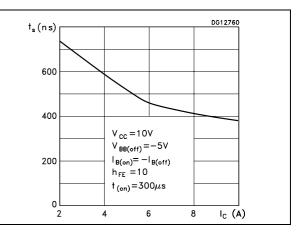
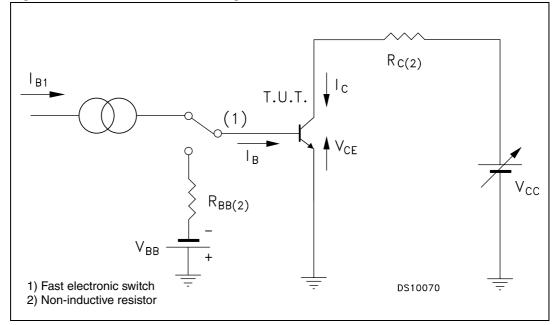


Figure 11. Inductive load switching time



## 2.2 Test circuit



### Figure 13. Resistive load switching test circuit

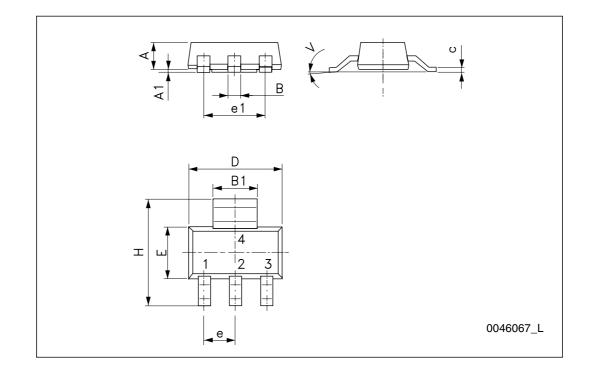


## 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and products status are available at: www.st.com. ECOPACK is an ST trademark.



	SOT-223 mechanical data					
DIM.		mm.				
DIW.	min.	typ	max.			
A			1.80			
A1	0.02		0.1			
В	0.60	0.70	0.85			
B1	2.90	3.00	3.15			
с	0.24	0.26	0.35			
D	6.30	6.50	6.70			
е		2.30				
e1		4.60				
E	3.30	3.50	3.70			
н	6.70	7.00	7.30			
V			10 °			





# 4 Revision history

### Table 5.Document revision history

Date	Revision	Changes
16-Mar-2009	1	First issue



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