

STPS1H100MF

High voltage power Schottky rectifier

Features

- Negligible switching losses
- High junction temperature capability
- Low leakage current
- Good trade-off between leakage current and forward voltage drop
- Avalanche capability specified

Description

Schottky rectifier designed for high frequency miniature switch mode power supplies such as adaptors and on-board DC/DC convertors. This device is packaged in STmite flat.

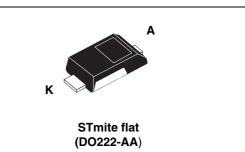


Table 1. Device summary

I _{F(AV)}	1 A
V _{RRM}	100 V
T _j (max)	175 °C
V _F (max)	0.62 V

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Characteristics

Table 2.	Absolute ratings (I	imiting values)

Parameter	Value	Unit	
Repetitive peak reverse voltage		100	V
Forward current rms		2	А
Average forward current	$T_{c} = 160 \ ^{\circ}C \ \delta = 0.5$	1	А
Surge non repetitive forward current	t _p = 10 ms sinusoidal	50	А
Repetitive peak reverse current $t_p = 2 \mu s$, F = I kHz square		1	А
Non-repetitive peak reverse current $t_p = 100 \ \mu s \ square$		1	А
Repetitive peak avalanche power	t _p = 1 μs T _j = 25 °C	1500	W
Storage temperature range	-65 to + 175	°C	
Maximum operating junction tempera	175	°C	
Critical rate of rise of reverse voltage	10000	V/µs	
	Repetitive peak reverse voltage Forward current rms Average forward current Surge non repetitive forward current Repetitive peak reverse current Non-repetitive peak reverse current Repetitive peak avalanche power Storage temperature range Maximum operating junction tempera	Repetitive peak reverse voltageForward current rmsAverage forward current $T_c = 160 \ ^{\circ}C \ \delta = 0.5$ Surge non repetitive forward current $t_p = 10 \ ^{\circ}ms \ ^{\circ}sinusoidal$ Repetitive peak reverse current $t_p = 2 \ ^{\circ}\mu s, \ F = 1 \ ^{\circ}kHz \ ^{\circ}square$ Non-repetitive peak reverse current $t_p = 100 \ ^{\circ}\mu s \ ^{\circ}quare$ Repetitive peak avalanche power $t_p = 1 \ ^{\circ}\mu s \ ^{\circ}T_j = 25 \ ^{\circ}C$	Repetitive peak reverse voltage100Forward current rms2Average forward current $T_c = 160 \ ^{\circ}C \ \delta = 0.5$ 1Surge non repetitive forward current $t_p = 10 \ ms \ sinusoidal$ 50Repetitive peak reverse current $t_p = 2 \ \mu s, \ F = I \ kHz \ square$ 1Non-repetitive peak reverse current $t_p = 100 \ \mu s \ square$ 1Repetitive peak avalanche power $t_p = 1 \ \mu s \ T_j = 25 \ ^{\circ}C$ 1500Storage temperature range-65 to + 175175

 $1. \quad \frac{dPtot}{dTj} < \frac{1}{Rth(j-a)} \text{ condition to avoid thermal runaway for a diode on its own heatsink}$

Table 3.Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case	20	°C/W

Table 4. Static electrical characteristics

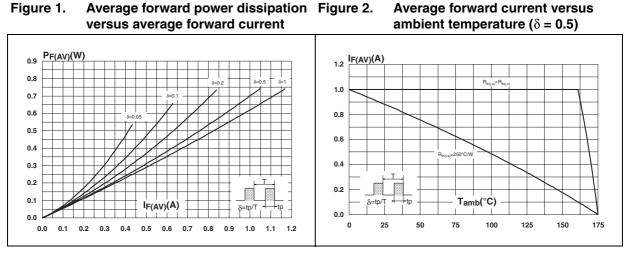
Symbol	Parameter	Tests conditions		Min.	Тур	Max.	Unit
ı (1)	I _R ⁽¹⁾ Reverse leakage current	T _j = 25 °C	V _R = V _{RRM}			4	μA
'R '		T _j = 125 °C			0.2	0.5	mA
	V (2) Forward valtage drap	T _j = 25 °C	I _F = 1 A			0.77	
V _F ⁽²⁾		T _j = 125 °C			0.58	0.62	v
V _F ⁽²⁾ Forward voltage drop	T _j = 25 °C	I _F = 2 A			0.86	v	
	T _j = 125 °C			0.65	0.7		

1. Pulse test: = 5 ms, δ < 2%

2. Pulse test: = 380 μ s, δ < 2%

To evaluate the conduction losses use the following equation: P = 0.54 x $I_{F(AV)}$ + 0.08 $I_{F}{}^{2}{}_{(RMS)}$





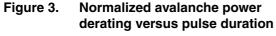


Figure 4. Normalized avalanche power derating versus junction temperature

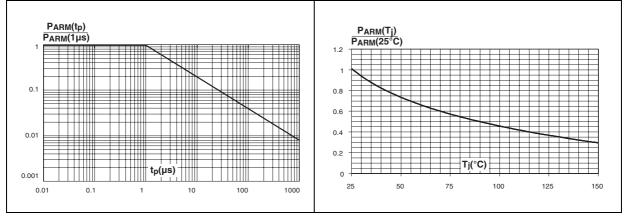
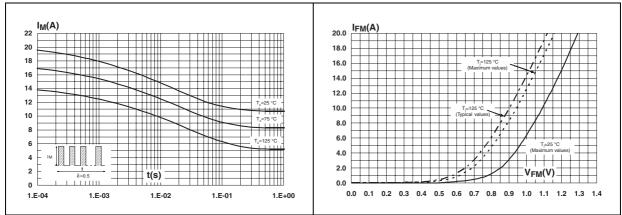


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values)

Figure 6. Forward voltage drop versus forward current



- Figure 7. Relative variation of thermal impedance, junction to ambient, versus pulse duration (epoxy printed circuit board, copper thickness = 35 µm, recommended pad layout)
- Figure 8. Thermal resistance, junction to ambient, versus copper surface under each lead (epoxy printed board FR4, copper thickness = 35 μm)

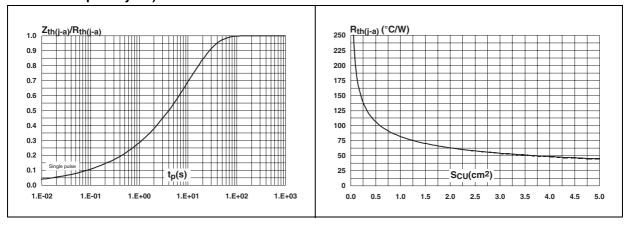
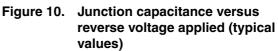
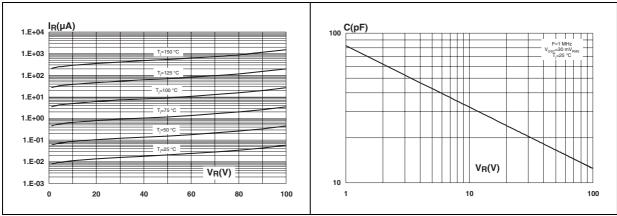


Figure 9. Reverse leakage current versus voltage applied (typical values)



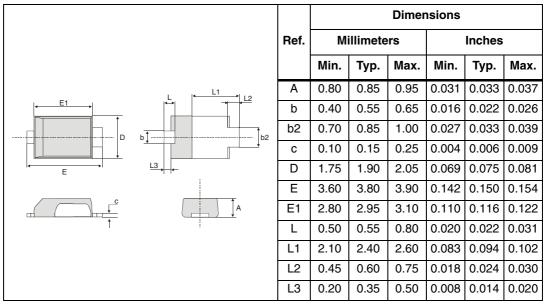


2 Package information

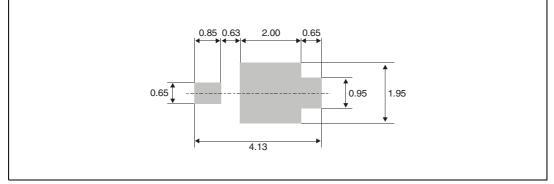
• Epoxy meets UL94, V0

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at *www.st.com*.

Table 5. STmite flat dimensions







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3 Ordering information

Table 6.Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS1H100MF	M11	STmite flat	16 mg	12000	Tape and reel

4 Revision history

Table 7.Document revision history

Date	Revision	Changes
15-May-2008	1	First issue.



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