TOSHIBA Diode Silicon Epitaxial Planar Type

1SS301

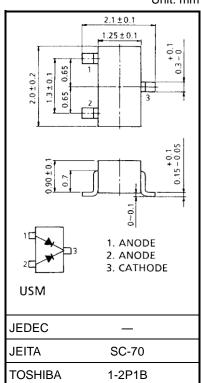
Ultra High Speed Switching Applications

- AEC-Q101 Qualified (Note1)
- Small package :SC-70
- Low forward voltage : V_F (3) = 0.9 V (typ.)
- Fast reverse recovery time: t_{rr} = 1.6 ns (typ.)
- Small total capacitance $: C_T = 0.9 \text{ pF} (typ.)$

Note1: For detail information, please contact to our sales.

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V _{RM}	85	V	
Reverse voltage	VR	80	V	
Maximum (peak) forward current	IFM	300 (*)	mA	
Average forward current	lo	100 (*)	mA	
Surge current (10 ms)	IFSM	2 (*)	А	
Power dissipation	Р	100	mW	
Junction temperature	Tj	125	°C	
Storage temperature	T _{stg}	-55 to 125	°C	



Weight: 0.006 g (typ.) Note: Using continuously under heavy loads (e.g. the application of high

temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

* Unit rating. Total rating = unit rating × 1.5

Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _{F (1)}	-	I _F = 1 mA	—	0.60		
	VF (2)	—	IF = 10 mA	—	0.72	-	V
	VF (3)	-	I _F = 100 mA	—	0.90	1.20	
Reverse current	I _{R (1)}	_	V _R = 30 V	_	_	0.1	μΑ
	IR (2)	_	VR = 80 V	_	_	0.5	
Total capacitance	CT	_	V _R = 0, f = 1 MHz	_	0.9	3.0	pF
Reverse recovery time	t _{rr}	_	I _F = 10 mA, Fig.1	_	1.6	4.0	ns

Start of commercial production 1986-11

Unit: mm

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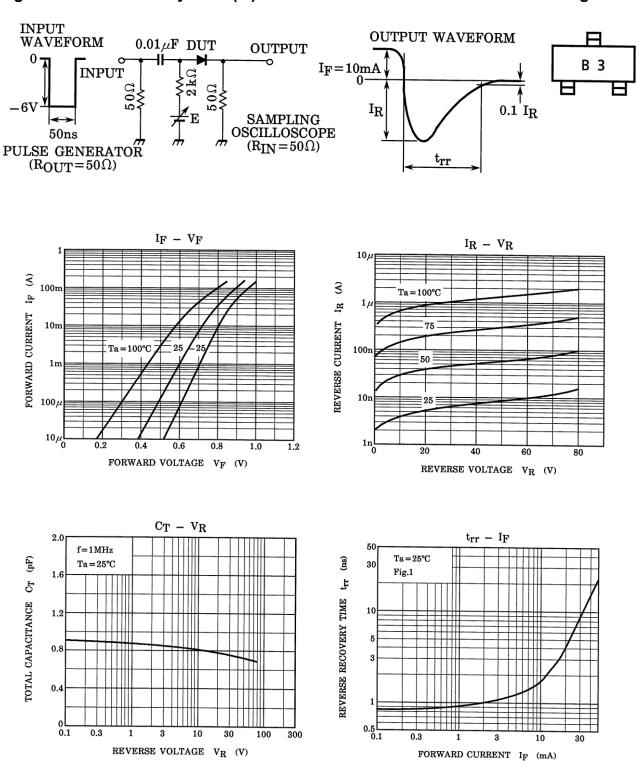


Fig.1 Reverse Recovery Time (trr) Test Circuit

Marking

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