Unit: mm

TOSHIBA Diode Silicon Epitaxial Planar Diode

1SS397

High Voltage, High Speed Switching Applications

• Small package : SC-70

 $\begin{array}{ll} \bullet & \text{Low forward voltage} & : V_F = 1.0V \text{ (typ.)} \\ \bullet & \text{High voltage} & : V_R = 400V \text{ (min)} \\ \bullet & \text{Fast reverse recovery time: } t_{rr} = 0.5 \mu s \text{ (typ.)} \\ \bullet & \text{Small total capacitance} & : C_T = 2.5 pF \text{ (typ.)} \\ \end{array}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse Voltage	V _{RM}	420	V	
Reverse voltage	V _R	400	V	
Maximum (peak) forward current	I _{FM}	300	mA	
Average forward current	lo	100	mA	
Surge current (10ms)	IFSM	2	Α	
Power dissipation	Р	100	mW	
Junction temperature	Tj	125	°C	
Storage temperature range	T _{stg}	−55 to 125	°C	

2.1±0.1 1.25±0.1 1.25±0.1 1.0+060 2.0+07 2.0+07 1. ANODE 2. N.C. USM 3. CATHODE JEDEC — JEITA SC-70 TOSHIBA 1-2P1D

Weight: 0.006g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high

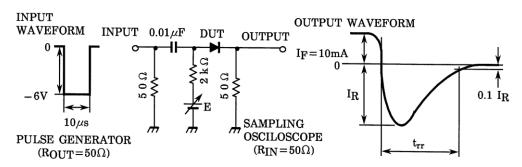
temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

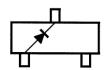
Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	VF (1)	I _F = 10mA	_	0.8	_	V
	V _{F (2)}	I _F = 100mA	_	1.0	1.3	
Reverse current	IR (1)	V _R = 300V	_	_	0.1	
	I _{R (2)}	V _R = 400V	_	_	1.0	μΑ
Total capacitance	Ст	$V_R = 0 V, f = 1MH_Z$	_	2.5	5.0	pF
Reverse recovery time	t _{rr}	I _F = 10mA (Fig.1)	_	0.5	_	μs

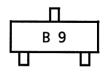
Fig.1 Reverse Recovery Time (trr) Test Circuit



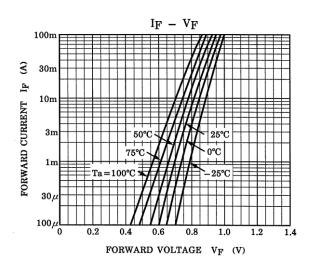
Equivalent Circuit (Top View)

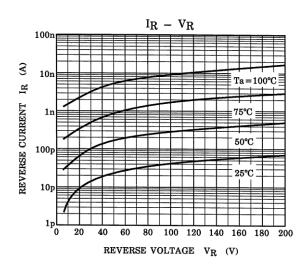


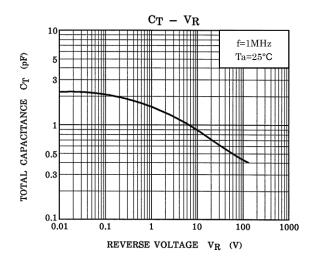
Marking

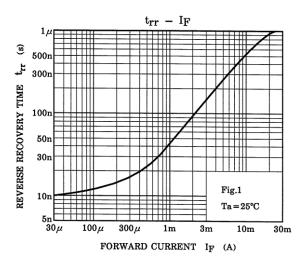


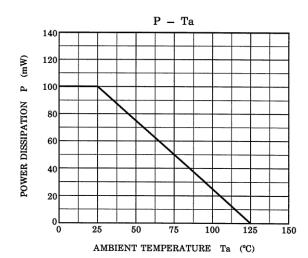
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