TOSHIBA

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

TK13A50DA

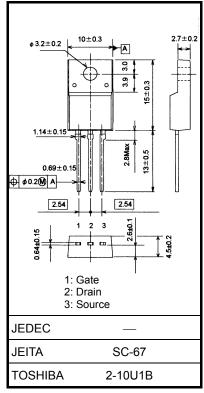
Switching Regulator Applications

- Low drain-source ON-resistance: R_{DS} (ON) = 0.39 Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 6.0 \text{ S}$ (typ.)
- Low leakage current: I_{DSS} = 10 μA (max) (V_{DS} = 500 V)

Absolute Maximum Ratings (Ta = 25°C)

• Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Characteristics Rating Unit Symbol Drain-source voltage VDSS 500 V Gate-source voltage VGSS ±30 V DC (Note 1) 12.5 ΙD Drain current Α Pulse (Note 1) 50 IDP Drain power dissipation (Tc = 25° C) 45 W P_{D} Single pulse avalanche energy E_{AS} 416 m.J (Note 2) 12.5 Avalanche current Α I_{AR} E_{AR} 4.5 Repetitive avalanche energy (Note 3) mJ °C Channel temperature T_{ch} 150 -55 to 150 °C Storage temperature range Tstg



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

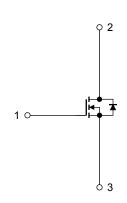
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 4.53 mH, R_G = 25 Ω , I_{AR} = 12.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Internal Connection



Start of commercial production 2008-09

Unit: mm

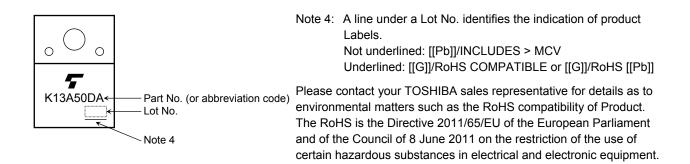
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rent	I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = 500 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	500		_	V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	-resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6.3 \text{ A}$		0.39	0.47	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6.3 \text{ A}$	1.5	6.0	_	S
Input capacitance	e	C _{iss}			1550	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		7	_	pF
Output capacitance		C _{oss}			165		
Switching time	Rise time	tr	V_{GS} $I_D = 6.3 \text{ A } V_{OUT}$		25	_	
	Turn-on time	t _{on}	$0 \vee - \mathbf{L} = \mathbf{R}_{L} = 32 \Omega$		60		ns
	Fall time	t _f	 		15		113
	Turn-off time	t _{off}	Duty \leq 1%, t _w = 10 μ s		110	_	
Total gate charge		Qg		_	28		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 12.5 \text{ A}$		18		nC
Gate-drain charge		Q _{gd}]	_	10		

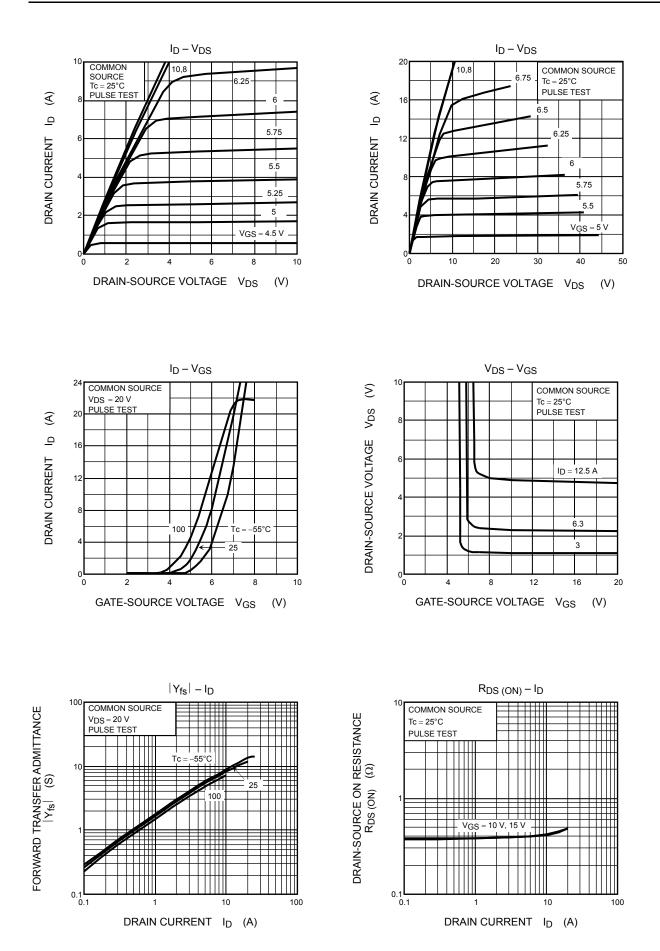
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	12.5	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	50	А
Forward voltage (diode)	V _{DSF}	$I_{DR} = 12.5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 12.5 \text{ A}, V_{GS} = 0 \text{ V},$	_	1300	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs		13	_	μC

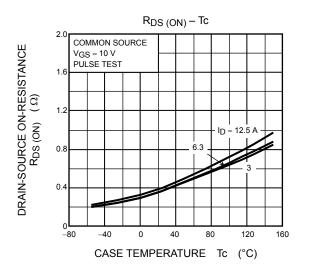
Marking

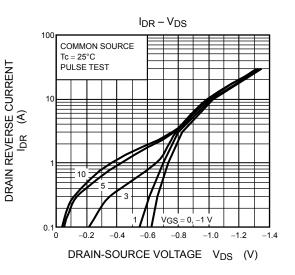


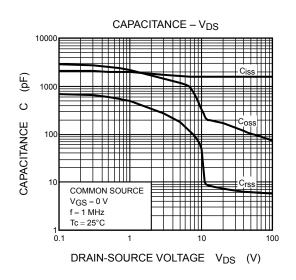
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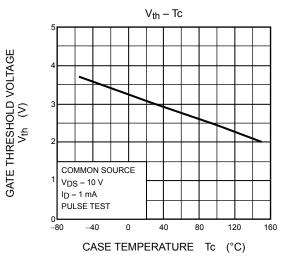


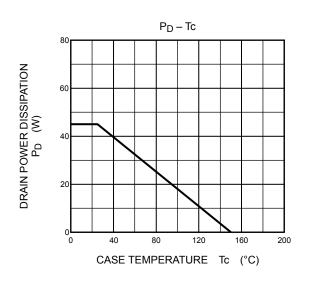
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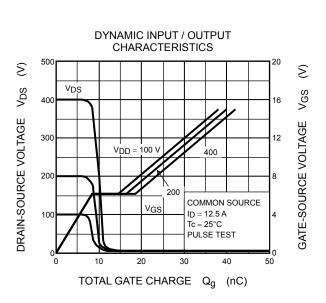


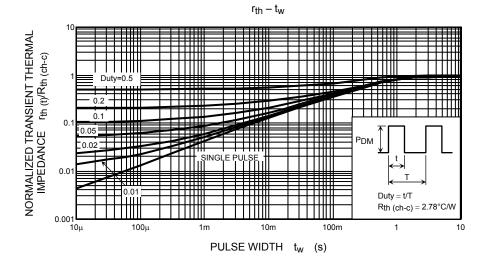


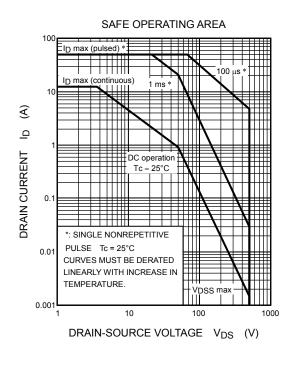


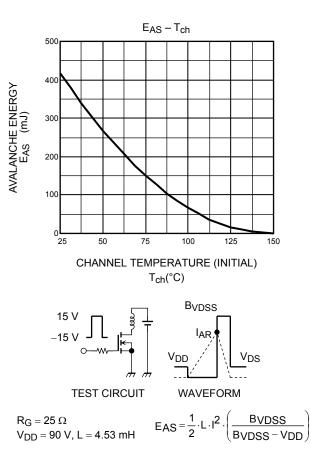












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