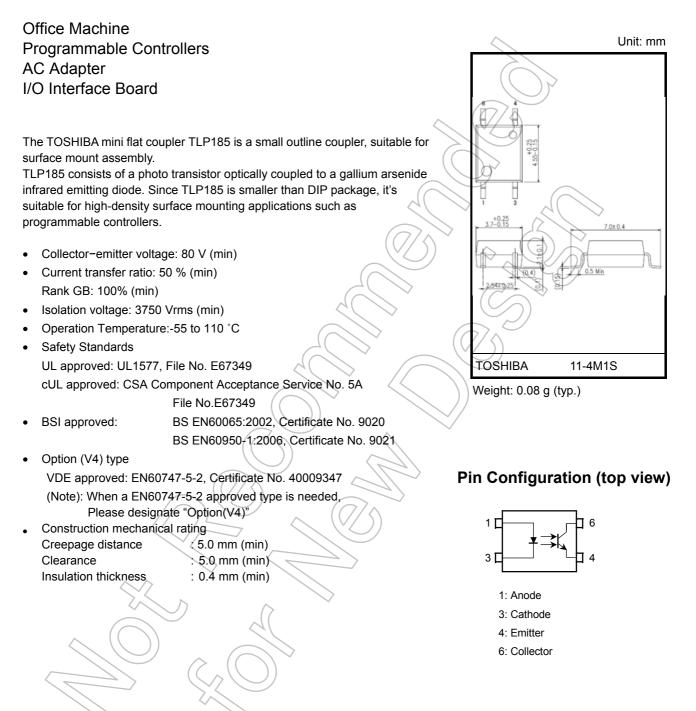
#### TLP185

**TOSHIBA** 

TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

# **TLP185**



# <u>TOSHIBA</u>

### **Current Transfer Ratio**

			sfer Ratio (%) / I <sub>F</sub> )				
Туре	Classification Note1	I <sub>F</sub> = 5mA, V <sub>CE</sub> = 5V, Ta = 25°C		Marking Of Classification			
		Min	Max				
	Blank	50	400	Blank, YE, GR, GB, Y+, G, G+, B			
	Rank Y	50	150	YE			
	Rank GR	100	300	GR			
TLP185	Rank GB	100	400	GB			
ILP 100	Rank YH	75	150	Y+			
	Rank GRL	100	200	G			
	Rank GRH	150	300	G+			
	Rank BLL	200	400	в			

(Note1): Ex Rank GB: TLP185 (GB,E

(Note) Application, type name for certification test, please use standard product type name, i, e. TLP185(GB,E: TLP185

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	IF	50	mA
	Forward current derating (Ta ≥ 90°C)	ΔI <sub>F</sub> / °C	-1.5	mA / °C
LED	Pulse forward current (Note2)	IFP	1	А
	Reverse voltage	V <sub>R</sub>	5	V
	Junction temperature	Tj	125	)°c
	Collector-emitter voltage	V <sub>CEO</sub>	80	v
	Emitter-collector voltage	V <sub>ECO</sub>	$\langle \langle \chi \rangle \rangle$	V
Detector	Collector current	Ι <sub>C</sub>	50	mA
Dete	Collector power dissipation	PC	150	mW
	Collector power dissipation derating $(Ta \ge 25^{\circ}C)$	ΔP <sub>C</sub> /°C	-1.5	mW/°C
	Junction temperature	Тј	125	°C
Оре	erating temperature range	Topr	-55 to 110	°C >
Sto	rage temperature range	T <sub>stg</sub>	-55 to 125	J.C.
Lea	d soldering temperature	T <sub>sol</sub>	260 (10s)	ç
Tota	al package power dissipation	PT	200	mW
Tota	al package power dissipation derating (Ta $\ge$ 25°C)	ΔP <sub>T</sub> / °C	-2.0	mW / °C
Isol	ation voltage (AC, 1minute, R.H. ≤ 60%) (Note 3)	BVS	3750	V <sub>rms</sub>

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Pulse width  $\leq$  100  $\mu$ s, f=100 Hz

Note 3: Device considered a two terminal device: Pins 1 and 3 shorted together and 4 and 6 shorted together.

### **Recommended Operating Conditions (Note)**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V <sub>CC</sub>	_	5	48	V
Forward current	│ IF	_	16	20	mA
Collector current	lc		1	10	mA

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

### Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.1	1.25	1.4	V
LED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	—	_	5	μA
	Capacitance	CT	V = 0, f = 1 MHz	$\nearrow$	30	_	pF
	Collector-emitter breakdown voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> = 0.5 mA	80	1	_	V
ŗ	Emitter-collector breakdown voltage	V <sub>(BR) ECO</sub>	I <sub>E</sub> = 0.1 mA	X	)/	_	V
Detector	Collector dark current	ICEO	V <sub>CE</sub> = 48 V	$\overline{2}$	0.01	0.08	μA
Ğ			V <sub>CE</sub> = 48 V, Ta = 85°C	$\mathcal{A}$	2	50	μA
	Capacitance (collector to emitter)	C <sub>CE</sub>	V = 0, f = 1 MHz		10	—	pF

### Coupled Electrical Characteristics (Ta = 25°C)

				AL		
Characteristic	Symbol	Test Condition	MIn	тур.	Max	Unit
Current transfer ratio	I <sub>C</sub> / I <sub>F</sub>	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V Rank GB	50 100	Ð	400 400	%
Saturated CTR	I <sub>C</sub> / I <sub>F (sat)</sub>	IF = 1 mA, V <sub>CE</sub> = 0.4 V Rank GB	30	<ul><li>60</li><li>—</li></ul>	_	%
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$I_{C} = 2.4 \text{ mA}, I_{F} = 8 \text{ mA}$ $I_{C} = 0.2 \text{ mA}, I_{F} = 1 \text{ mA}$	) =	— 0.2	0.3	V
Off-state collector current	IC (off)	Rank GB V <sub>F</sub> = 0.7 V, V <sub>CE</sub> = 48 V		1	0.3 10	μA

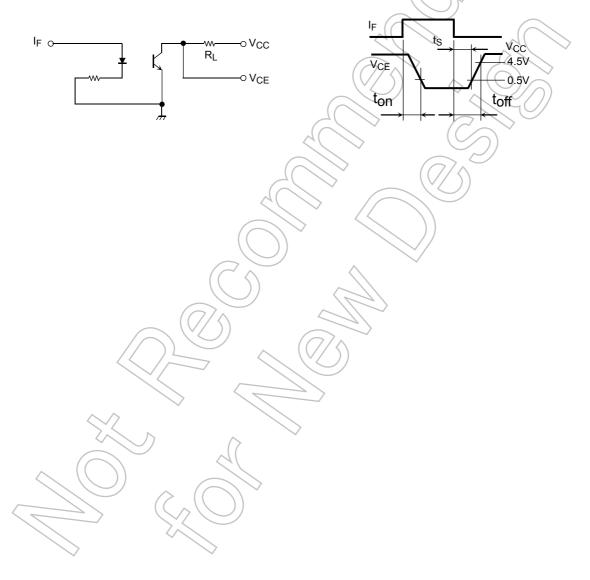
### Isolation Characteristics (Ta $= 25^{\circ}$ C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	CS	V <sub>S</sub> = 0V, f = 1 MHz	_	0.8	—	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60%	1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
		AC, 1 minute	3750	_	-	V
Isolation voltage	BVs	AC, 1 second, in oil	_	10000	_	V <sub>rms</sub>
		DC, 1 minute, in oil	_	10000	_	V <sub>dc</sub>

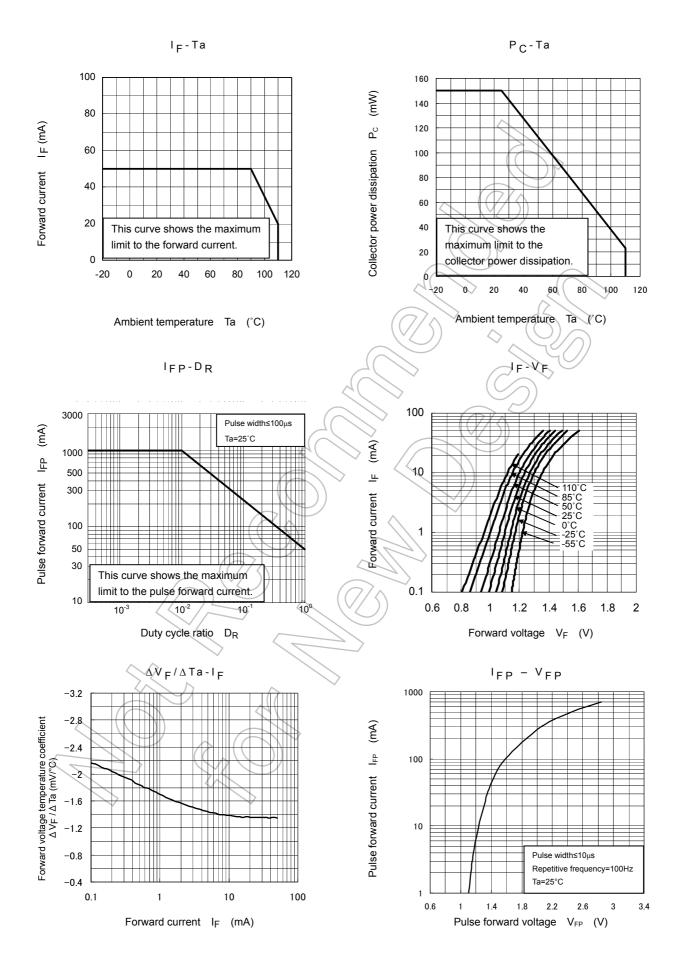
Switching Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t <sub>r</sub>	$V_{CC}$ = 10 V, I <sub>C</sub> = 2 mA R <sub>L</sub> = 100Ω	—	5	—	μs
Fall time	t <sub>f</sub>		_	9	_	
Turn-on time	t <sub>on</sub>		$\nearrow$	9	_	
Turn-off time	t <sub>off</sub>			9	_	
Turn–on time	t <sub>on</sub>		F	) /2	_	
Storage time	ts		$7 \times$	30	—	μS
Turn-off time	t <sub>off</sub>		Ð	70		

#### Fig. 1 Switching time test circuit



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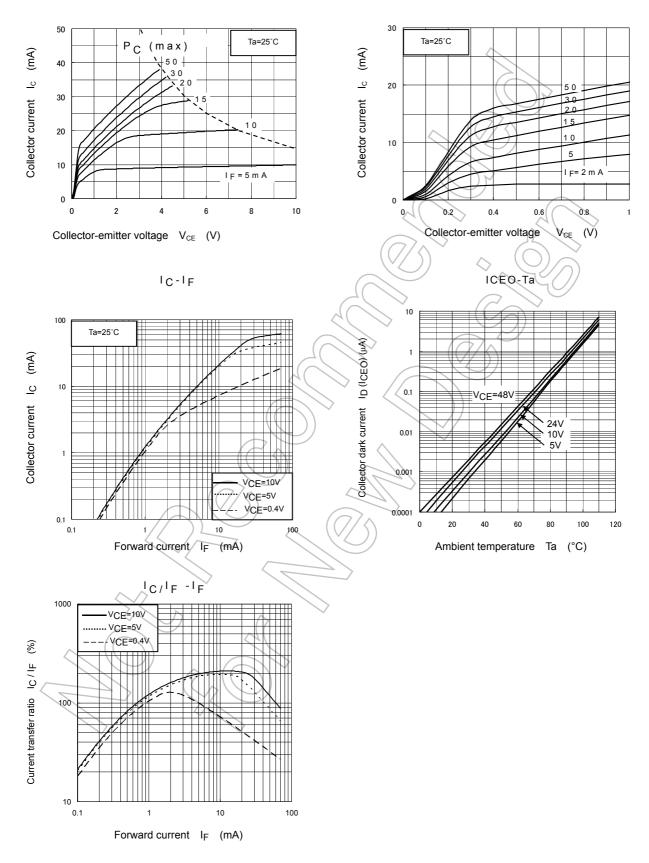


\*The above graphs show typical characteristic.

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IC-VCE

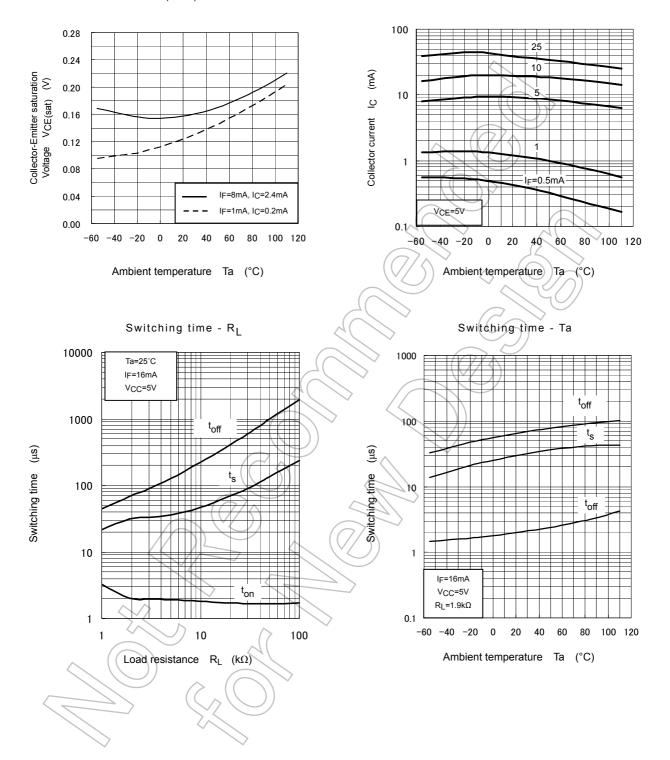




<sup>\*</sup>The above graphs show typical characteristic.

V<sub>CE(sat)</sub> – Ta

I<sub>С</sub> - Та



\*The above graphs show typical characteristic.

### **Soldering and Storage**

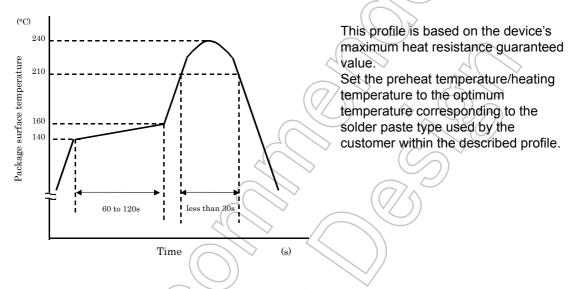
#### 1. Soldering

1.1 Soldering

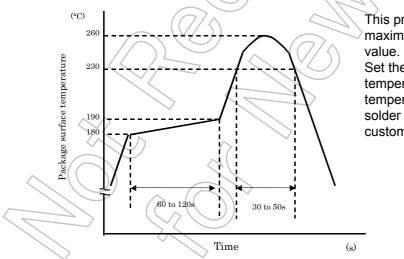
When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as

much as possible by observing the following conditions.

- 1) Using solder reflow
  - ·Temperature profile example of lead (Pb) solder



·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

Please preheat it at 150°C between 60 and 120 seconds.

Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.

3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

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#### 2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.

3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.

- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

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