Unit: mm

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSVI)

TPC6110

Power Management Switch Applications

• Small footprint due to small and thin package

• Low drain-source ON resistance: $RDS(ON) = 43 \text{ m}\Omega$ (typ.)

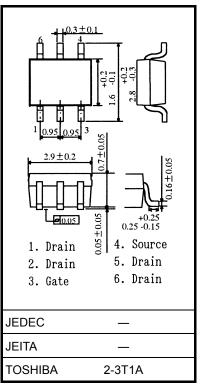
• Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$

• Enhancement mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V}$

 $(V_{DS} = -10 \text{ V}, I_{D} = -0.1 \text{mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	-30	V	
Drain-gate voltage (R	$GS = 20 \text{ k}\Omega$)	V _{DGR}	-30	V	
Gate-source voltage		V _{GSS}	-25/+20	V	
Drain current	DC (Note 1) I _D	-4.5	Α	
Diain current	Pulse (Note 1) I _{DP}	-18	A	
Drain power dissipation	on (t = 5 s (Note 2a		2.2	W	
Drain power dissipation	on (t = 5 s (Note 2b		0.7	W	
Single pulse avalanch	e energy (Note 3) E _{AS}	3.4	mJ	
Avalanche current		I _{AR}	-2.3	Α	
Repetitive avalanche	energy (Note 4) E _{AR}	0.025	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	



Weight: 0.011 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

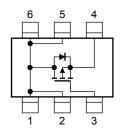
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	R _{th (ch-a)}	56.8	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R _{th (ch-a)}	178.5	°C/W

Note: (Note 1), (Note 2), (Note 3), (Note 4) and (Note 5): See other pages.

This transistor is an electrostatic-sensitive device. Please handle with caution.

Circuit Configuration



Start of commercial production 2009-08



Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	— ±100		±100	nA	
Drain cut-off curre	ent	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			μА		
Drain-source breakdown voltage		V _{(BR) DSS}	$I_D = -10$ mA, $V_{GS} = 0$ V	-30	_	_	V	
		V _{(BR) DSX}	$I_D = -10 \text{ mA}, V_{GS} = 10 \text{ V}$ (Note 7)	-21	_	_	V	
Gate threshold vo	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -0.1 \text{ mA}$	-0.8	_	-2.0	٧	
Drain-source ON resistance		D	$V_{GS} = -4.5 \text{ V}, I_D = -2.2 \text{ A}$	_	59	77	mΩ	
		R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -2.2 \text{ A}$	_	43	56	1117.5	
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -2.2 \text{ A}$	4.2	8.4	_	S	
Input capacitance		C _{iss}		_	510	_	pF	
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	85	_		
Output capacitance		C _{oss}		_	110	_		
Switching time	Rise time	t _r	V_{CS} 0 V Γ $I_D = -2.2 \text{ A}$	_	6	_	- ns	
	Turn-on time	t _{on}	V _{GS} -10 V I _D = -2.2 A O V _{OUT} G S S S S S S S S S	_	12	_		
	Fall time	t _f	R _L = 6.	_	21	_		
	Turn-off time	t _{off}	$V_{DD} \approx -15 \text{ V}$ Duty ≤ 1%, $t_W = 10 \text{ μs}$	_	70	_		
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ -24 V, V _{GS} = -10 V,	_	14		nC	
Gate-source charge 1		Q _{gs1}	$I_D = -4.5 \text{ A}$		1.6			
Gate-drain ("miller") charge		Q _{gd}		_	3.8	_		

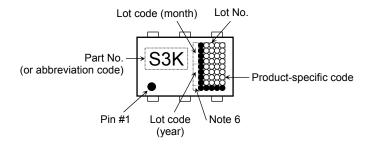
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics Symbol		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	_	_	_	-18	Α
Forward voltage (diode) V _{DSF}		V _{DSF}	$I_{DR} = -4.5 \text{ A}, V_{GS} = 0 \text{ V}$	_		1.2	V

Note 7: VDSX mode (the application of a plus voltage between gate and source) may cause decrease in maximum rating of drain-source voltage.

2

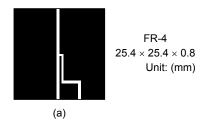
Marking (Note 5)

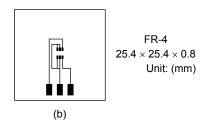


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a) (t = 5 s)

(b) Device mounted on a glass-epoxy board (b) (t = 5 s)





Note 3: $V_{DD} = -24 \text{ V}$, $T_{ch} = 25 ^{\circ}\text{C}$ (initial), L = 0.5 mH, $R_G = 25 \Omega$, $I_{AR} = -2.3 \text{ A}$

Note 4: Repetitive rating : pulse width limited by maximum channel temperature

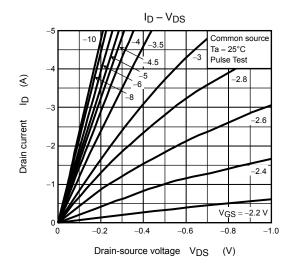
Note 5: • on lower left of the marking indicates Pin 1.

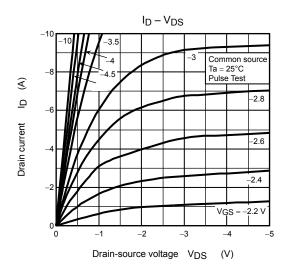
Note 6: A dot marking for identifying the indication of product Labels.

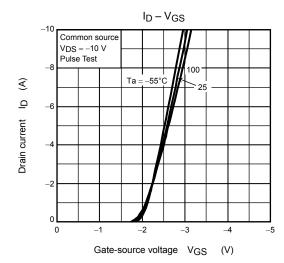
Without a dot: [[Pb]]/INCLUDES > MCV

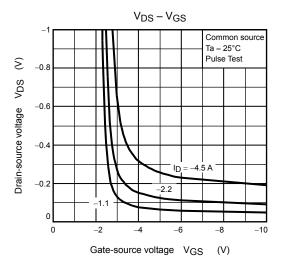
With a dot: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

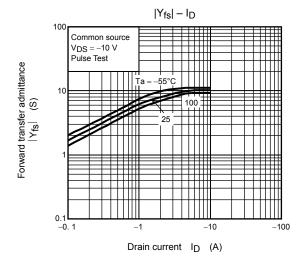
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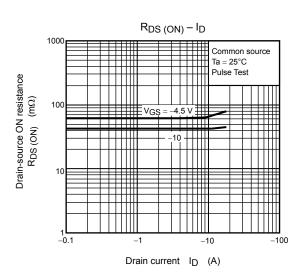


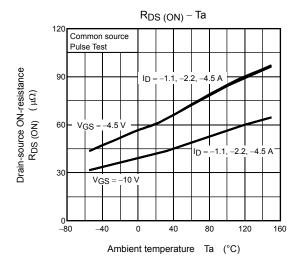


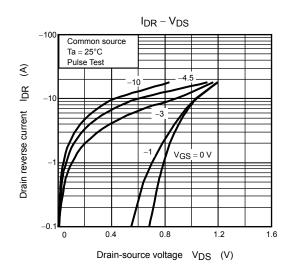


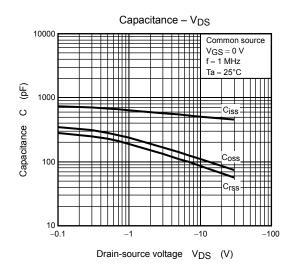


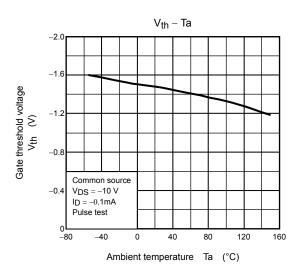


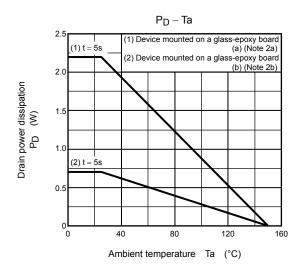


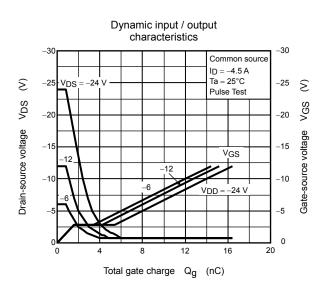


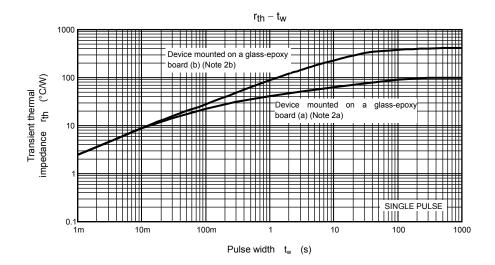


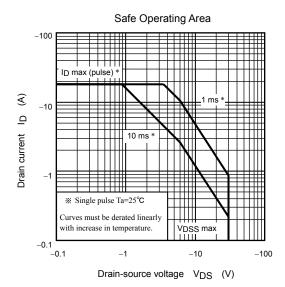












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