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## Vishay Semiconductors

# **Small Signal Fast Switching Diode**



#### **FEATURES**

- Silicon epitaxial planar diode
- · Fast switching diodes
- AEC-Q101 qualified
- Base P/N-E3 RoHS-compliant, commercial grade



- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **MECHANICAL DATA**

Case: SOD-123

Weight: approx. 10.3 mg
Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE						
PART	ORDERING CODE TYPE MARKING INTERNA		INTERNAL CONSTRUCTION	REMARKS		
1N4148W	1N4148W-E3-08 or 1N4148W-E3-18	A2	Single diade	Tape and reel		
	1N4148W-HE3-08 or 1N4148W-HE3-18	AZ	Single diode			

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		$V_{R}$	75	V	
Repetitive peak reverse voltage		$V_{RRM}$	100	V	
Average rectified current half wave rectification with resistive load <sup>(1)</sup>	f ≥ 50 Hz	I <sub>F(AV)</sub>	150	mA	
Curan formand augment	t <sub>p</sub> < 1 s	I <sub>FSM</sub>	500	mA	
Surge forward current	t <sub>p</sub> = 1 μs	I <sub>FSM</sub>	2	А	
Power dissipation (1)		P <sub>tot</sub>	350	mW	

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	357	K/W		
Junction temperature		T <sub>j</sub>	150	°C		
Storage temperature range		T <sub>stg</sub>	- 65 to + 150	°C		
Operating temperature range		Top	- 55 to + 150	°C		

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature.



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>			1	V	
Forward voltage	I <sub>F</sub> = 100 mA	$V_{F}$			1.2	V	
	V <sub>R</sub> = 20 V	I <sub>R</sub>			25	nA	
Leakage current	V <sub>R</sub> = 75 V	I <sub>R</sub>			5	μA	
Leakage current	V <sub>R</sub> = 100 V	$I_{R}$			100	μΑ	
	V <sub>R</sub> = 20 V, T <sub>J</sub> = 150 °C	I <sub>R</sub>			50	μA	
Diode capacitance	$V_F = V_R = 0 V$	C <sub>D</sub>			4	pF	
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1 \mu s$ , rise time < 30 ns, $f_p = (5 \text{ to } 100) \text{ kHz}$	V <sub>fr</sub>			2.5	V	
Reverse recovery time	$I_F$ = 10 mA, $i_R$ = 1 mA, $V_R$ = 6 V, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			4	ns	

#### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

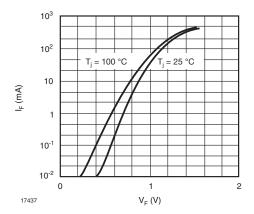


Fig. 1 - Forward Characteristics

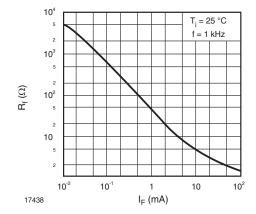


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

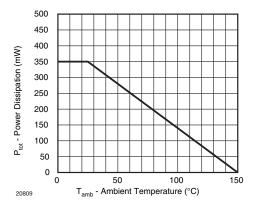


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

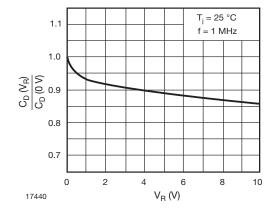


Fig. 4 - Relative Capacitance vs. Reverse Voltage



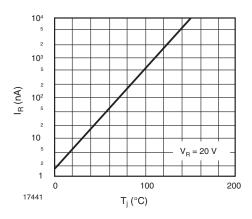


Fig. 5 - Leakage Current vs. Junction Temperature

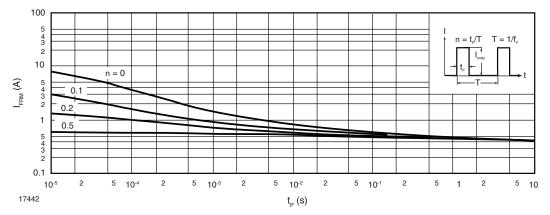
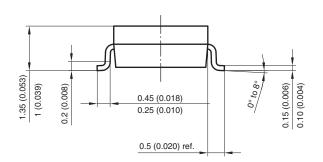


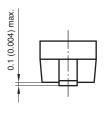
Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration



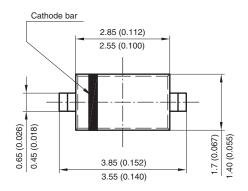
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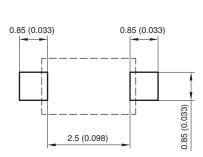
### PACKAGE DIMENSIONS in millimeters (inches): SOD-123





Mounting Pad Layout





Rev. 4 - Date: 24. Sep. 2009 Document no.: S8-V-3910.01-001 (4) 17432



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Revision: 13-Jun-16 1 Document Number: 91000

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<u>1N4148W-GS08</u> <u>1N4148W-V-GS18</u> <u>1N4148W-V-GS08</u> <u>1N4148W-HE3-18</u> <u>1N4148W-E3-08</u> <u>1N4148W-E3-18</u> <u>1N4148W-E3-18</u>