



N-Channel 150-V (D-S) 175 °C MOSFET

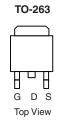
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)			
150	0.038 at V _{GS} = 10 V	40			
	0.042 at V _{GS} = 6 V	38			

FEATURES

- TrenchFET® Power MOSFETs
- 175 °C Junction Temperature
- New Low Thermal Resistance Package
- PWM Optimized
- Compliant to RoHS Directive 2002/95/EC



· Primary Side Switch





Ordering Information: SUM40N15-38-E3 (Lead (Pb)-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_C = 25 ^{\circ}C$, unless other	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	150	V		
Gate-Source Voltage	V _{GS}	± 20	V		
Continuous Drain Current (T _{.I} = 175 °C)	T _C = 25 °C	L	40	Δ.	
Continuous Diam Current (1) = 175 C)	T _C = 125 °C	- ID	23		
Pulsed Drain Current	I _{DM}	80	Α		
Avalanche Current	I _{AR}	40			
Repetitive Avalanche Energy ^a	L = 0.1 mH	E _{AR}	80	mJ	
	T _C = 25 °C	В	166 ^b	14/	
Maximum Power Dissipation ^a	T _A = 25 °C ^c	$ P_{D}$	3.75	W	
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount TO-263°)	R _{thJA}	40	°C/W		
Junction-to-Case (Drain)	R _{thJC}	0.9	C/VV		

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{DS} = 0 \text{ V, } I_{D} = 250 \mu\text{A}$	150			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			50		
		V _{DS} = 120 V, V _{GS} = 0 V, T _J = 175 °C			250		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	80			Α	
		V _{GS} = 10 V, I _D = 15 A		0.030	0.038		
Dunin Course On Chata Basistanasi	D	V _{GS} = 6 V, I _D = 10 A		0.033	0.042	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 15 A, T _J = 125 °C			0.076		
		V _{GS} = 10 V, I _D = 15 A, T _J = 175 °C			0.100		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A	10			S	
Dynamic ^b				•	-		
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		2500		pF	
Output Capacitance	C _{oss}			290			
Reverse Transfer Capacitance	C _{rss}			190			
Gate Resistance	R_{g}			2		Ω	
Total Gate Charge ^c	Q_{g}			38	60		
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 75 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$		13		nC	
Gate-Drain Charge ^c	Q_{gd}			13			
Turn-On Delay Time ^c	t _{d(on)}			15	25		
Rise Time ^c	t _r	$V_{DD} = 75 \text{ V}, R_{L} = 1.80 \Omega$		130	200		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 40 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		30	45	ns	
Fall Time ^c	t _f			90	140		
Source-Drain Diode Ratings and Cha	aracteristics	Γ _C = 25 °C ^b					
Continuous Current	Is				40		
Pulsed Current	I _{SM}				80	Α	
Forward Voltage ^a	V _{SD}	I _F = 40 A, V _{GS} = 0 V		1.0	1.5	V	
Reverse Recovery Time	t _{rr}			100	150	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 40 A, dl/dt = 100 A/μs		5	8	Α	
Reverse Recovery Charge	Q _{rr}			0.25	0.6	μC	

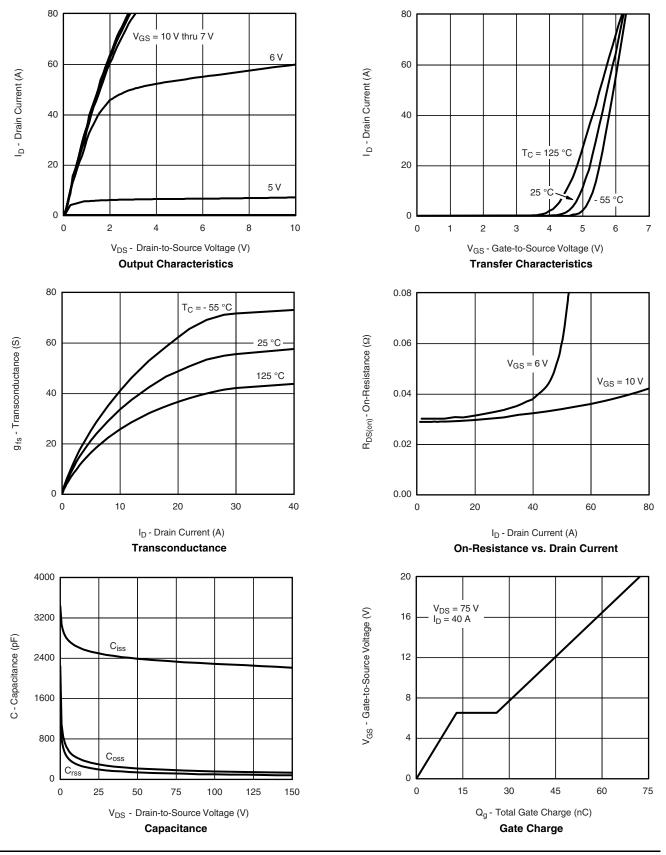
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



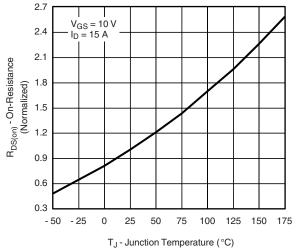
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



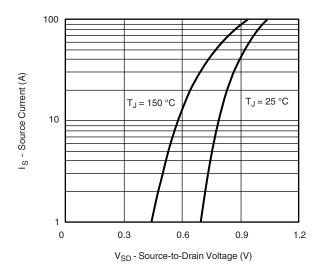
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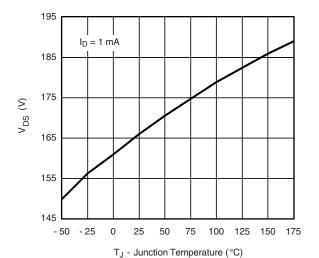
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Source-Drain Diode Forward Voltage

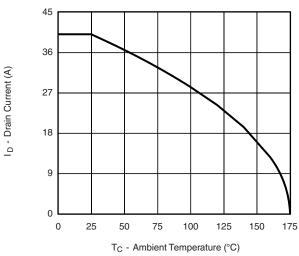


Drain Source Breakdown vs. Junction Temperature

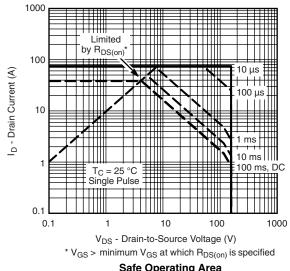




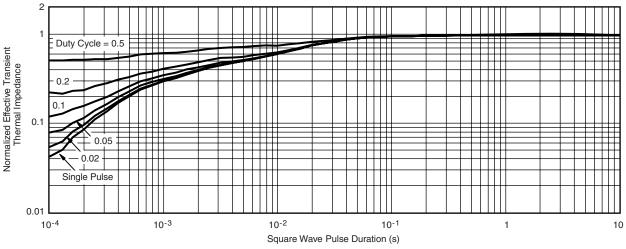
THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature







Normalized Thermal Transient Impedance, Junction-to-Case

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TO-263 (D²PAK): 3-LEAD









DETAIL A (ROTATED 90°)



_ - b1 , , ,	
≥ 	- -

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max. 8 mils.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. *: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

6. This feature is for thick lead.

		INCHES		MILLIMETERS			
DIM.		MIN.	MAX.	MIN.	MAX.		
А		0.160	0.190	4.064	4.826		
	b	0.020	0.039	0.508	0.990		
	b1	0.020	0.035	0.508	0.889		
	b2	0.045	0.055	1.143	1.397		
c*	Thin lead	0.013	0.018	0.330	0.457		
C	Thick lead	0.023	0.028	0.584	0.711		
c1	Thin lead	0.013	0.017	0.330	0.431		
CI	Thick lead	0.023	0.027	0.584	0.685		
	c2	0.045	0.055	1.143	1.397		
	D	0.340	0.380	8.636	9.652		
	D1	0.220	0.240	5.588	6.096		
	D2	0.038	0.042	0.965	1.067		
D3		0.045	0.055	1.143	1.397		
	D4	0.044	0.052	1.118	1.321		
	Е	0.380	0.410	9.652	10.414		
	E1	0.245	-	6.223	=		
	E2	0.355	0.375	9.017	9.525		
	E3	0.072	0.078	1.829	1.981		
е		0.100) BSC	2.54	4 BSC		
K		0.045	0.055	1.143	1.397		
L		0.575	0.625	14.605	15.875		
L1		0.090	0.110	2.286	2.794		
L2		0.040	0.055	1.016	1.397		
L3		0.050	0.070	1.270	1.778		
	L4	0.010) BSC	0.254	BSC		
	М	-	0.002	-	0.050		
ECN: T13-0707-Rev. K, 30-Sep-13							

DWG: 5843





RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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