V15P45-M3, V15P45HM3

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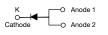
High Current Density Surface Mount Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.31$ V at $I_F = 5$ A

TMBS[®] eSMP[®] Series

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PRIMARY CHARACTERISTICS				
I _{F(AV)}	15 A			
V _{RRM}	45 V			
I _{FSM}	210 A			
V _F at I _F = 15 A	0.42 V			
T _J max.	150 °C			
Package	TO-277A (SMPC)			
Diode variations	Single die			

FEATURES

- Very low profile typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

MECHANICAL DATA

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 gualified

Base P/NHM3_X - halogen-free, RoHS-compliant, and AEC-Q101 qualified

("_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V15P45	UNIT	
Device marking code		V1545		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum DC forward current	I _F ⁽¹⁾	15	- A	
Maximum DC forward current	I _F ⁽²⁾	4.8		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	210	А	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Notes

⁽¹⁾ Mounted on 30 mm x 30 mm pad areas aluminum PCB

⁽²⁾ Free air, mounted on recommended copper pad area

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ROHS COMPLIANT

HALOGEN



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5.0 A	T _A = 25 °C	V _F ⁽¹⁾	0.40	-	V
	I _F = 7.5 A			0.45	-	
	I _F = 15 A			0.49	0.58	
	I _F = 5.0 A	T _A = 125 °C		0.31	-	
	I _F = 7.5 A			0.34	-	
	I _F = 15 A			0.42	0.51	
Reverse current	V _B = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	1500	μA
	v _R = 45 V	T _A = 125 °C	'R (²)	15	50	mA

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V15P45	UNIT	
Turping thermal registering	R _{0JA} ⁽¹⁾	75	°C/W	
Typical thermal resistance	R _{0JM} ⁽²⁾	4	0/11	

Notes

 $^{(1)}$ Free air, mounted on recommended copper pad area; thermal resistance $R_{\theta JA}$ - junction to ambient

 $^{(2)}$ Mounted on 30 mm x 30 mm aluminum PCB; thermal resistance $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V15P45-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V15P45-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	
V15P45HM3/86A (1)	0.10	86A	1500	7" diameter plastic tape and reel	
V15P45HM3/87A (1)	0.10	87A	6500	13" diameter plastic tape and reel	
V15P45HM3_A/H ⁽¹⁾	0.10	Н	1500	7" diameter plastic tape and reel	
V15P45HM3_A/I ⁽¹⁾	0.10	I	6500	13" diameter plastic tape and reel	

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

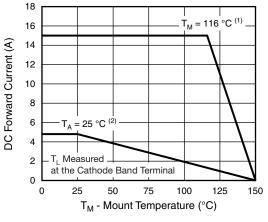


Fig. 1 - Forward Current Derating Curve

Notes

- $^{(1)}$ Mounted on 30 mm x 30 mm aluminum PCB; T_M measured at the terminal of cathode band (R_{0JM} = 4 °C/W)
- $^{(2)}$ Free air, mounted on recommended copper pad area (R $_{0JA}$ = 75 $^{\circ}C/W)$

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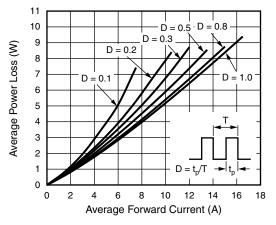


Fig. 2 - Forward Power Loss Characteristics Per Diode

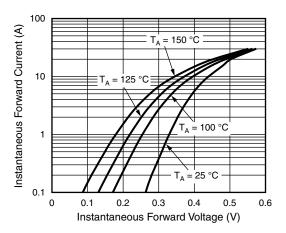


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

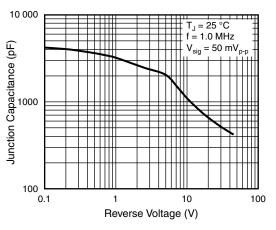


Fig. 5 - Typical Junction Capacitance

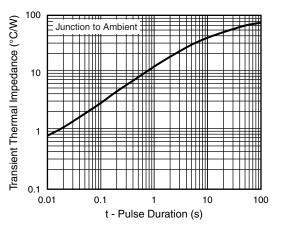


Fig. 6 - Typical Transient Thermal Impedance Per Diode

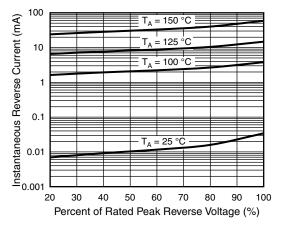


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

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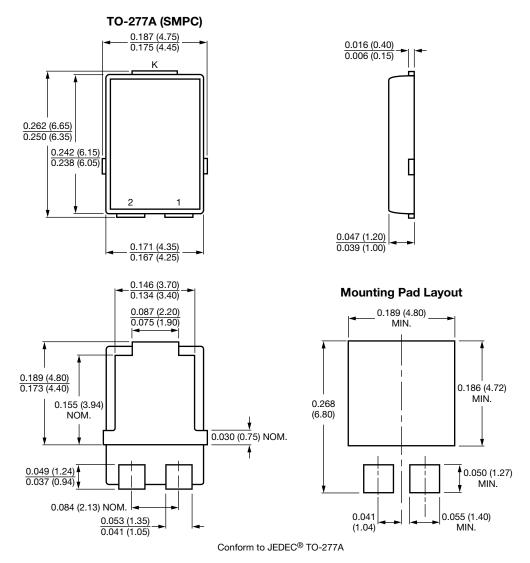
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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