**Vishay Semiconductors** 

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## High Performance Schottky Rectifier, 175 A





PowerTab<sup>®</sup>

PRODUCT SUMMARY				
Package	PowerTab <sup>®</sup>			
I <sub>F(AV)</sub>	175 A			
V <sub>R</sub>	45 V			
V <sub>F</sub> at I <sub>F</sub>	0.7 V			
I <sub>RM</sub>	640 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
Diode variation	Single die			
E <sub>AS</sub>	40 mJ			

### FEATURES

- 150 °C max. operating junction temperature
- High frequency operation
- Ultralow forward voltage drop
- Continuous high current operation
- Guard ring for enhanced ruggedness and long term reliability
- Screw mounting only
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-175BGQ045 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for low voltage output in high current AC/DC power supplies.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
1	Rectangular waveform	175	А		
I <sub>F(AV)</sub>	T <sub>C</sub>	103	°C		
V <sub>RRM</sub>		45	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	8700	А		
V <sub>F</sub>	175 A <sub>pk</sub> (typical)	0.63	V		
VF	TJ	150	°C		
TJ	Range	-55 to +150	C°		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	175BGQ045	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	45	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	40	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_{C}$ = 103 °C, rectangular waveform 175		175	А
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	8700	A
	IFSM	10 ms sine or 6 ms rect. pulse		1550	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 6 A, L = 2 mH		40	mJ
Repetitive avalanche current	I <sub>AR</sub>			А	

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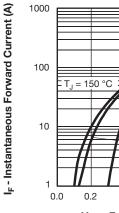
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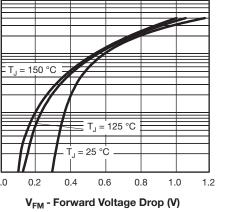
ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	100 A	T <sub>J</sub> = 25 °C	0.55	0.58	
Forward voltage drop		175 A		0.67	0.75	v
Forward voltage drop		100 A	- T <sub>J</sub> = 150 °C	0.49	0.54	
		175 A		0.63	0.7	
	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = 45 V		1200	2000	
Reverse leakage current		T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.6	2	mA
		T <sub>J</sub> = 125 °C		360	640	
Maximum junction capacitance	CT	$V_{R}$ = 5 $V_{DC}$ , (test signal range 100 kHz to 1 MHz) 25 °C		56	00	pF
Typical series inductance	L <sub>S</sub>	Measured from tab to mounting plane		3	.5	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000			000	V/µs

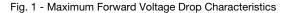
#### Note

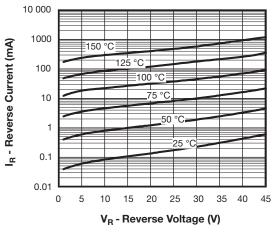
 $^{(1)}$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

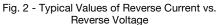
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and temperature range	storage	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resist junction to case	tance,	R <sub>thJC</sub>	DC operation	0.25	°C/W
Typical thermal resistan case to heatsink	ice,	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.20	
Approximate weight				5	g
				0.18	oz.
Mounting torque	minimum			1.2 (10)	N·m
	maximum			2.4 (20)	(lbf $\cdot$ in)
Marking device	Marking device Case style PowerTab <sup>®</sup> 175BG		GQ045		











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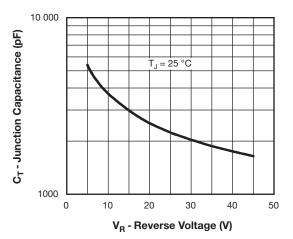


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

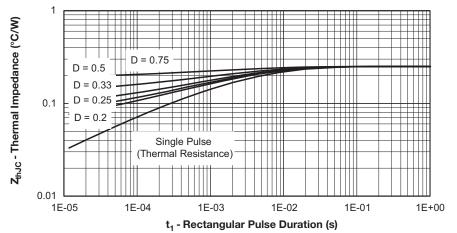


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

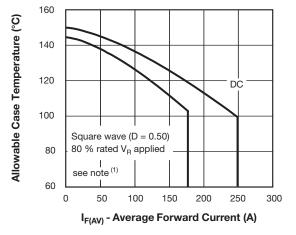
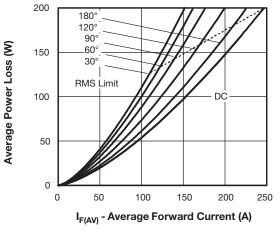
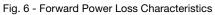


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current





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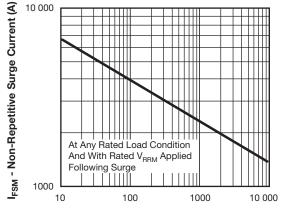
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## VS-175BGQ045

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t - Square Wave Pulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current

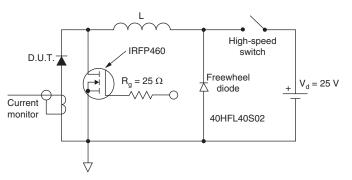


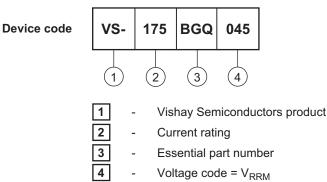
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward power loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \text{ at } (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \text{ (see fig. 6);} \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse power loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} (1 - \mathsf{D}); \, \mathsf{I}_{\mathsf{R}} \text{ at } \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \text{ rated } \mathsf{V}_{\mathsf{R}} \end{array}$ 

#### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95240</u>				
Part marking information www.vishay.com/doc?95370				
Application note	www.vishay.com/doc?95179			

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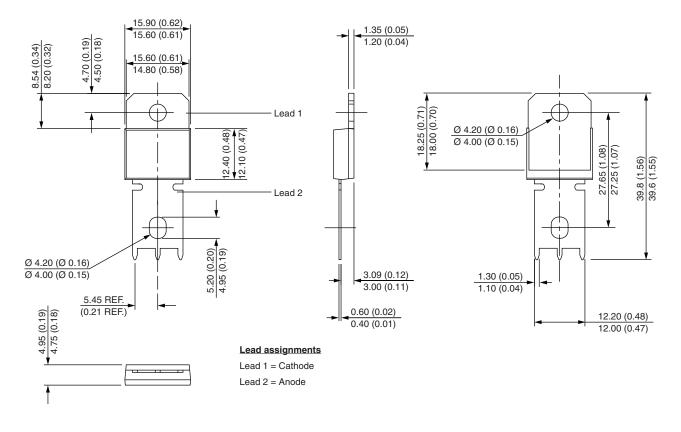
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### **DIMENSIONS** in millimeters (inches)





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